INTERNET OF THINGS				
(Effective from the academic year 2018 -2019) SEMESTER – VIII				
Course Code	18CS81	CIE Marks	40	
Number of Contact Hours/Week	3:0:0	SEE Marks	60	
Total Number of Contact Hours	40	Exam Hour	·s 03	
	CREDIT	S –3		
Course Learning Objectives: This cours	e (18CS81) w	ill enable students to:		
• Assess the genesis and impact of I	IoT applicatio	ns, architectures in real	world.	
• Illustrate diverse methods of deple	oying smart of	bjects and connect them	to network.	
Compare different Application pro	otocols for Io	Г.		
• Infer the role of Data Analytics an	nd Security in	IoT.		
• Identifysensor technologies for s	sensing real v	world entities and under	rstand the role	of IoT in
various domains of Industry.	-			
Module 1				Contact Hours
What is IoT Genesis of IoT IoT and Di	gitization IoT	Impact Convergence of	of IT and IoT	08
IoT Challenges IoT Network Architec	ture and Des	sign Drivers Rehind N	Jew Network	00
Architectures Comparing IoT Architectu	tures A Simpl	ified IoT Architecture '	The Core IoT	
Functional Stack JoT Data Management	and Compute	Stack		
Textbook 1. Ch 1 2		Stuck.		
RBT: L1, L2, L3				
Module 2				
Smart Objects: The "Things" in IoT,	Sensors, Ac	tuators, and Smart Ob	ojects, Sensor	08
Networks, Connecting Smart Objects, Communications Criteria, IoT Access Technologies.			chnologies.	
Textbook 1: Ch.3, 4				
RBT: L1, L2, L3				
Module 3				0.0
IP as the IoT Network Layer, The Bu	isiness Case	for IP, The need for	Optimization,	08
Transport Lawer LoT Application Transport	ompliances,	Application Protocols	for for, the	
Transport Layer, lo l'Application Transport Methods.				
$\begin{array}{c} 1 \in \mathcal{L}(D) \\ 0 \in \mathcal{L}($				
Module 4				
Data and Analytics for IoT An Introduc	tion to Data	Analytics for IoT Mach	ine Learning	08
Big Data Analytics Tools and Technolog	v Edge Stre	aming Analytics Netwo	ork Analytics	00
Securing IoT A Brief History of OT Security	urity Commo	n Challenges in OT Sec	urity How IT	
and OT Security Practices and Systems	Vary Formal	Risk Analysis Structur	es: OCTAVE	
and FAIR The Phased Application of Sec	urity in an Or	erational Environment		
Textbook 1. Ch 7 8	unty in an Op			
RBT: L1. L2. L3				
Module 5				
IoT Physical Devices and Endpoints - A	Arduino UNC	: Introduction to Ardu	ino. Arduino	08
UNO. Installing the Software. Fundament	als of Arduin	Programming.	IoT Physical	
Devices and Endpoints - RaspherryPi: In	troduction to	RaspberryPi. About the	e RaspberrvPi	
Board: Hardware Lavout, Operating Systems on RaspberryPi. Configuring RaspberryPi				
Programming RaspberryPi with Python, V	Wireless Tem	perature Monitoring Sys	tem Using Pi,	
DS18B20 Temperature Sensor, Connecti	ng Raspberry	Pi via SSH, Accessing	Temperature	
from DS18B20 sensors, Remote access to	RaspberryPi	, Smart and Connected (Cities, An IoT	

Strategy for Smarter Cities, Smart City IoT Architecture, Smart City Security Architecture, Smart City Use-Case Examples.

Textbook 1: Ch.12

Textbook 2: Ch.7.1 to 7.4, Ch.8.1 to 8.4, 8.6

RBT: L1, L2, L3

Course Outcomes: The student will be able to :

- Interpret the impact and challenges posed by IoT networks leading to new architectural models.
- Compare and contrast the deployment of smart objects and the technologies to connect them to network.
- Appraise the role of IoT protocols for efficient network communication.
- Elaborate the need for Data Analytics and Security in IoT.
- Illustrate different sensor technologies for sensing real world entities and identify the applications of IoT in Industry.

Question Paper Pattern:

- The question paper will have ten questions.
- Each full Question consisting of 20 marks
- There will be 2 full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Textbooks:

 David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Robert Barton, Jerome Henry, "IoT Fundamentals: Networking Technologies, Protocols, and Use Cases for the Internet of Things", 1stEdition, Pearson Education (Cisco Press Indian Reprint). (ISBN: 978-9386873743)
 Srinivasa K G, "Internet of Things", CENGAGE Leaning India, 2017

Reference Books:

- 1. Vijay Madisetti and ArshdeepBahga, "Internet of Things (A Hands-on-Approach)", 1stEdition, VPT, 2014. (ISBN: 978-8173719547)
- 2. Raj Kamal, "Internet of Things: Architecture and Design Principles", 1st Edition, McGraw Hill Education, 2017. (ISBN: 978-9352605224)

Mandatory Note:

Distribution of CIE Marks is a follows (Total 40 Marks):

- 20 Marks through IA Tests
- 20 Marks through practical assessment

Maintain a copy of the report for verification during LIC visit.

Posssible list of practicals:

- 1. Transmit a string using UART
- 2. Point-to-Point communication of two Motes over the radio frequency.
- 3. Multi-point to single point communication of Motes over the radio frequency.LAN (Subnetting).
- 4. I2C protocol study
- 5. Reading Temperature and Relative Humidity value from the sensor

MOBILE COMPUTING				
(Effective from the academic year 2018 -2019) SEMESTER – VIII				
Course Code	18CS821	CIE Marks	40	
Number of Contact Hours/Week	3:0:0	SEE Marks	60	
Total Number of Contact Hours	40	Exam Hours	03	
	CREDITS	-3		
Course Learning Objectives: This cours	e (18CS821) wi	ll enable students to:		
 Define concepts of wireless comm 	nunication.			
Compare and contrast propagation	n methods, Char	nnel models, capacity calcula	tions mul	ltiple
antennas and multiple user technic	ques used in the	mobile communication.		
• Explain CDMA, GSM. Mobile IP	, WImax and D	ifferent Mobile OS		
Illustrate various Markup Langua	ges CDC, CLDO	C, MIDP; Programming for C	CLDC, M	IDlet
model and security concerns				
Module 1				Contact
				Hours
Mobile Computing Architecture: Archite	ecture for Mob	ile Computing, 3-tier Archi	tecture,	08
Design Considerations for Mobile Comp	uting. Emerging	Technologies: Wireless bro	adband	
(WiMAX), Mobile IP: Introduction, disco	overy, Registrat	ion, Tunneling, Cellular IP,	Mobile	
IP with IPv6. Wireless Networks : Global	Systems for M	obile Communication (GSM): GSM	
Architecture, Entities, Call routing in GSN	M, PLMN Interf	face, GSM Addresses and Ide	entities,	
Network Aspects in GSM, Mobility Man	agement, GSM	Frequency allocation. Short	Service	
Messages (SMS): Introduction to SMS	S, SMS Archit	ecture, SMMT, SMMO, S	SMS as	
Information bearer, applications				
Textbook1: 2.4 - 2.6, 4.4 - 4.6, 5, 6.				
RBT: L1, L2	RBT: L1. L2			
Module 2				
GPRS and Packet Data Network, GPRS	Network Archit	ecture, GPRS Network Ope	rations,	08
Data Services in GPRS, Applications for	or GPRS, Billir	ng and Charging in GPRS.	Spread	
Spectrum technology, IS-95, CDMA	versus GSM,	Wireless Data, Third Ger	neration	
Networks, Applications on 3G, Mobile	Client: Moving	g bevond desktop. Mobile	handset	
overview. Mobile phones and their feat	ures. PDA. Des	ign Constraints in applicati	ons for	
handheld devices				
Textbook 1: 7.9.2 - 9.7. 12.2 - 12.6				
RBT: L1. L2				
Module 3				
Mobile OS and Computing Environme	nt: Smart Clie	nt Architecture. The Clien	t: User	08
Interface, Data Storage, Performance, Da	ata Synchroniza	tion, Messaging, The Serve	er: Data	
Synchronization. Enterprise Data Source	. Messaging. N	lobile Operating Systems: V	WinCE.	
Palm OS, Symbian OS, Linux, Proprie	etary OS Clien	t Development: The devel	opment	
process Need analysis phase. Design pha	se Implementat	ion and Testing phase Depl	ovment	
phase Development Tools Device Emula	tors		oʻj illollo	
Textbook 2: 7. 8.				
RBT: L1. L2				
Module 4				
Building Wireless Internet Applications	Thin client	overview. Architecture the	client	08
Middleware, messaging Servers, Proces	ssing a Wirele	ss request. Wireless Appl	ications	

Protocol (WAP) Overview, Wireless Languages: Markup Languages, HDML, WML, 10				
Hours HTML, cHTML, XHTML, VoiceXML.				
Textbook 2: 11, 12, 13				
RBT: L1, L2				
Module 5				
J2ME: Introduction, CDC, CLDC, MIDP; Programming for CLDC, MIDlet model,	08			
Provisioning, MIDlet life-cycle, Creating new application, MIDlet event handling, GUI in				
MIDP, Low level GUI Components, Multimedia APIs; Communication in MIDP, Security				
Considerations in MIDP.				
Textbook 1: 15.1 - 15.10				
RBT: L1, L2				
Course Outcomes: The student will be able to :				
The students shall able to:				
• Explain state of art techniques in wireless communication.				
• Discover CDMA, GSM. Mobile IP, WImax				
• Demonstrate program for CLDC, MIDP let model and security concerns				
Question paper pattern:				
The question paper will have ten questions.				
There will be 2 questions from each module.				
Each question will have questions covering all the topics under a module.				
The students will have to answer 5 full questions, selecting one full question from each module.				
Text Books:				
1. Ashok Talukder, Roopa Yavagal, Hasan Ahmed: Mobile Computing, Technology, Appl	lications			
and Service Creation, 2nd Edition, Tata McGraw Hill, 2010.				
2. Martyn Mallik: Mobile and Wireless Design Essentials, Wiley India, 2003				
Reference Books:				
1. Raj kamal: Mobile Computing, Oxford University Press, 2007.				
2. Iti Saha Misra: Wireless Communications and Networks, 3G and Beyond, Tata McGraw Hill,				
2009.				

STORAGE AREA NETWORKS			
(Effective from the academic year 2018 -2019) SEMESTER – VII			
Course Code	18CS822	CIE Marks	40
Number of Contact Hours/Week	3:0:0	SEE Marks	60
Total Number of Contact Hours	40	Exam Hours	03
	CREDITS –3		
Course Learning Objectives: This course	e (18CS822) will en	able students to:	
• Evaluate storage architectures,			
• Define backup, recovery, disaster	recovery, business c	continuity, and replication	
• Examine emerging technologies in	ncluding IP-SAN		
• Understand logical and physical c	omponents of a stor	age infrastructure	
• Identify components of managing	and monitoring the	data center	
 Define information security and id 	lentify different stor	age virtualization technol	ogies
Module 1	centry different stor		Contact
Would 1			Hours
Storage System: Introduction to Inform	nation Storage. Inf	ormation Storage Evolut	ion of 08
Storage Architecture Data Center Infrast	nation Storage. In neture Virtualizatio	on and Cloud Computing	Data
Center Environment : Application	atabase Managem	ent System (DBMS)	Host
(Compute) Connectivity Storage Disk	Drive Components	Disk Drive Performance	Host
Access to Data Direct-Attached Storage	Storage Design Bas	ed on Application	, 1105t
Textbook1 : $Ch.1.1$ to 1.4 . $Ch.2.1$ to 2.1)	ed on Appheation	
RRT: L1. L2			
Module 2			
Data Protection - RAID · RAID Impleme	entation Methods R	AID Array Components	
Tachniques DAID Levels DAID Impediation Methods, KAID Array Components, KAID			rison
Intelligent Storage Systems · Compon	ents of an Intellig	ent Storage System Typ	uison.
Intelligent Storage Systems Fibre Channel Storage Area Networks - Fibre Channel:			annel
Overview The SAN and Its Evolution Co	mponents of FC SA	N	
Textbook1 · Ch 31 to 36 Ch 41 43 Ch 51 to 53			
RRT. I 1 I 2			
Module 3			
IP SAN and FCoE: iSCSI FCIP Netw	vork-Attached Sto	rage: General-Purpose S	ervers 08
versus NAS Devices. Benefi ts of NAS. Fi	le Systems and Net	work File Sharing. Comp	onents
of NAS, NAS I/O Operation, NAS Impl	ementations. NAS	File-Sharing Protocols. F	Tactors
Affecting NAS Performance		, -	
Textbook1 : Ch.6.1. 6.2. Ch. 7.1 to 7.8			
RBT: L1, L2			
Module 4			
Introduction to Business Continuity:	Information Avail	ability, BC Terminology	y, BC 08
Planning Life Cycle, Failure Analysis, Bu	siness Impact Analy	ysis, BC Technology Solu	itions,
Backup and Archive: Backup Purpo	se, Backup Consid	lerations, Backup Granu	larity,
Recovery Considerations, Backup Methods, Backup Architecture. Backup and Restore			estore
Operations, Backup Topologies, Backup in NAS Environments			
Textbook1 : Ch.9.1 to 9.6, Ch. 10.1 to 1	0.9		
RBT: L1, L2			
Module 5			
Local Replication: Replication Terminol	ogy, Uses of Local	Replicas, Replica Consist	ency, 08
Local Replication Technologies, Trackin	ng Changes to Sou	irce and Replica, Restor	e and
Restart Considerations, Creating Multiple	Replicas. Remote	Replication: Modes of R	emote

Replication, Remote Replication Technologies. **Securing the Storage Infrastructure:** Information Security Framework, Risk Triad, Storage Security Domains. Security Implementations in Storage Networking

Textbook1 : Ch.11.1 to 11.7, Ch. 12.1, 12.2, Ch. 14.1 to 14.4

RBT: L1, L2

Course Outcomes: The student will be able to :

- Identify key challenges in managing information and analyze different storage networking technologies and virtualization
- Explain components and the implementation of NAS
- Describe CAS architecture and types of archives and forms of virtualization
- Illustrate the storage infrastructure and management activities

Question Paper Pattern:

- The question paper will have ten questions.
- Each full Question consisting of 20 marks
- There will be 2 full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Textbooks:

1. EMC Education Services, **"Information Storage and Management**", Wiley India Publications, 2009. ISBN: 9781118094839

Reference Books:

1. Paul Massiglia, Richard Barker, "Storage Area Network Essentials: A Complete Guide to Understanding and Implementating SANs Paperback", 1st Edition, Wiley India Publications, 2008

NOSQL DATABASE				
(Effective from the academic year 2018 -2019)				
Correct Code	SEMESTER 1900922	– VIII CIE Maalaa	40	
Course Code	180.8823	CIE Marks	40	
Total Number of Contact Hours	3:0:0	SEE Marks	00	
Total Number of Contact Hours		2 Exam nours	05	
Course Learning Objectives. This course	$\frac{\mathbf{CREDITS}}{\mathbf{e} (18\mathbf{CS823}) \mathbf{w}}$	-3 ill enable students to:		
• Define compare and use the four	types of NoSO	In chable students to.	ionted Kov	Value
Pairs Column-oriented and Grant	types of NoSQ	L Databases (Document-or	ienieu, Key	v alue
 Demonstrate an understanding of 	the detailed are	hitecture define objects lo	nad data Juu	erv data
and performance tune Column-ori	iented NoSOL	latabases	bad data, qu	cry data
 Explain the detailed architecture 	define objects	load data, query data and n	erformance	tune
Document-oriented NoSOL datab	ases	ioad data, query data and p	cirormanee	tune
Module 1	4505.			Contact
				Hours
Why NoSOL? The Value of Relational I	Databases. Gett	ing at Persistent Data. Cor	ncurrency.	08
Integration, A (Mostly) Standard Model,	Impedance M	smatch, Application and I	ntegration	00
Databases, Attack of the Clusters, The Em	nergence of No	SOL.	0	
Aggregate Data Models; Aggregates, Ex	ample of Relat	ions and Aggregates, Con	sequences	
of Aggregate Orientation, Key-Value and	d Document D	ata Models, Column-Fami	ly Stores,	
Summarizing Aggregate-Oriented Databa	ses.			
More Details on Data Models; Relatio	nships, Graph	Databases, Schemaless I	Databases,	
Materialized Views, Modeling for Data A	ccess,			
Textbook1: Chapter 1,2,3				
RBT: L1, L2, L3				
Module 2				
Distribution Models; Single Server, S	sharding, Mas	er-Slave Replication, Pe	er-to-Peer	08
Replication, Combining Sharding and Rep	plication.			
Consistency, Update Consistency, Read	d Consistency,	Relaxing Consistency,	The CAP	
Theorem, Relaxing Durability, Quorums.			. 1	
Version Stamps, Business and System Transactions, Version Stamps on Multiple Nodes			odes	
1 extbook1: Chapter 4,5,6				
KD1: L1, L2, L3 Modulo 3				
Man Reduce Basic Man Reduce Partit	tioning and C	mbining Composing Ma	n Reduce	08
Calculations A Two Stage Man-Reduce F	Fyample Increa	mental Man-Reduce	ip-Reduce	08
Key-Value Databases What Is a Key-Va	alue Store Key	-Value Store Features Co	nsistency	
Transactions Overy Features Structure of	f Data Scaling	Suitable Use Cases Storir	ng Session	
Information User Profiles Preference St	opping Cart D	ata When Not to Use Rel	ationships	
among Data, Multioperation Transactions	. Ouery by Dat	a. Operations by Sets	utonships	
Textbook1: Chapter 7.8	, C j . j	.,		
RBT: L1, L2, L3				
Module 4				
Document Databases, What Is a Docume	ent Database?,	Features, Consistency, Tra	nsactions,	08
Availability, Query Features, Scaling.	Suitable Use	Cases, Event Logging.	Content	
Management Systems, Blogging Platfor	rms, Web An	lytics or Real-Time Ana	lytics. E-	
Commerce Applications. When Not to	Use, Comple	Transactions Spanning	Dif erent	
Operations, Queries against Varving Agon	egate Structure	e - r8		
Textbook1: Chapter 9	J			

RBT: L1, L2, L3			
Module 5			
Graph Databases, What Is a Graph Database?, Features, Consistency, Transactions, 08			
Availability, Query Features, Scaling, Suitable Use Cases, Connected Data, Routing,			
Dispatch, and Location-Based Services, Recommendation Engines, When Not to Use.			
Textbook1: Chapter 11			
RBT: L1, L2, L3			
Course Outcomes: The student will be able to :			
• Define, compare and use the four types of NoSQL Databases (Document-oriented, KeyValue			
Pairs, Column-oriented and Graph).			
• Demonstrate an understanding of the detailed architecture, define objects, load data, query data			
and performance tune Column-oriented NoSQL databases.			
• Explain the detailed architecture, define objects, load data, query data and performance tune			
Document-oriented NoSQL databases.			
Question Paper Pattern:			
• The question paper will have ten questions.			
Each full Question consisting of 20 marks			
• There will be 2 full questions (with a maximum of four sub questions) from each module.			
• Each full question will have sub questions covering all the topics under a module.			
• The students will have to answer 5 full questions, selecting one full question from each module.			
Textbooks:			
1. Sadalage, P. & Fowler, NoSQL Distilled: A Brief Guide to the Emerging World of Polyglot			
Persistence, Pearson Addision Wesley, 2012			
Reference Books:			
1. Dan Sullivan, "NoSQL For Mere Mortals", 1st Edition, Pearson Education India, 2015. (ISBN-			
13: 978-9332557338)			
2. Dan McCreary and Ann Kelly, "Making Sense of NoSQL: A guide for Managers and the Rest of			
us", 1st Edition, Manning Publication/Dreamtech Press, 2013. (ISBN-13: 978-9351192022)			
3. Kristina Chodorow, "Mongodb: The Definitive Guide- Powerful and Scalable Data Storage", 2nd			
Edition, O'Reilly Publications, 2013. (ISBN-13: 978-9351102694)			

MULTICORE ARCHITECTURE AND PROGRAMMING			
(Effective fro	om the academic year 2	2018 -2019)	
SEMESTER – VII			
Course Code	18CS824	CIE Marks	40
Number of Contact Hours/Week	3:0:0	SEE Marks	60
Total Number of Contact Hours	40	Exam Hours	03
	CREDITS –3		
Course Learning Objectives: This course	e (18CS824) will enable	students to:	
Define technologies of multicore a	architecture and perform	ance measures	
 Demonstrate problems related to r 	nultiprocessing		
Illustrate windows threading, posi	x threads, openmp prog	ramming	
Analyze the common problems in	parallel programming		
Module -1			Contact Hours
Introduction to Multi-core Architecture	Motivation for Concur	rency in software Par	allel 08
Computing Platforms Parallel Computing	in Microprocessors	Differentiating Multi-	ore
Architectures from Hyper- Threading Te	chnology Multi-thread	ling on Single-Core ve	
Multi-Core Platforms Understanding P	erformance Amdahl's	Law Growing Retu	rns.
Gustafson's Law System Overview of	Threading · Defining	Threads System View	of
Threads Threading above the Operating	System Threads inside	the OS Threads inside	the
Hardware, What Happens When a Thread	Is Created. Application	Programming Models	and
Threading, Virtual Environment: VMs	and Platforms. Runtin	me Virtualization. Svs	tem
Virtualization.	,	,	
Textbook 1: Ch.1. 2			
RBT: L1. L2. L3			
Module -2			
Fundamental Concepts of Parallel	Programming :Design	ing for Threads. 7	ask 08
Decomposition Data Decomposition Data Flow Decomposition Implications of Different			rent
Decompositions, Challenges You'll Fac	e, Parallel Programmi	ng Patterns, A Motiva	ting
Problem: Error Diffusion, Analysis of	the Error Diffusion	Algorithm, An Altern	nate
Approach: Parallel Error Diffusion, Other	Alternatives. Threading	g and Parallel Programm	ning
Constructs: Synchronization, Critical S	Sections, Deadlock, S	ynchronization Primiti	ves,
Semaphores, Locks, Condition Variables,	Messages, Flow Contr	ol- based Concepts, Fe	nce,
Barrier, Implementation-dependent Threading Features			
Textbook 1: Ch.3, 4			
RBT: L1, L2, L3			
Module – 3			
Threading APIs :ThreadingAPIs for M	Aicrosoft Windows, V	Vin32/MFC Thread A	Pls, 08
Threading APIs for Microsoft. NET Fr	amework, Creating Th	reads, Managing Thre	ads,
Thread Pools, Thread Synchronization,	POSIX Threads, Cre	eating Threads, Manag	ging
Threads, Thread Synchronization, Signalin	ng, Compilation and Lin	king.	
Textbook 1: Ch.5			
RBT: L1, L2, L3			
Module-4			
OpenMP: A Portable Solution for Threadi	ng : Challenges in Thre	ading a Loop, Loop-car	ried 08
Dependence, Data-race Conditions, Mana	ging Shared and Private	Data, Loop Scheduling	and
Portioning, Effective Use of Reductions, Minimizing Threading Overhead, Work-sharing			ring
Sections, Performance-oriented Program	ming, Using Barrier	and No wait, Interleav	/ing
Single-thread and Multi-thread Execution,	Data Copy-in and Copy	-out, Protecting Update	s of
Shared Variables, Intel Task queuing Ex	tension to OpenMP, C	penMP Library Function	ons,

OpenMP Environment Variables, Compilation, Debugging, performance Textbook 1: Ch.6 RBT: L1, L2, L3 Module-5 Solutions to Common Parallel Programming Problems : Too Many Threads, Data Races, 08 Deadlocks, and Live Locks, Deadlock, Heavily Contended Locks, Priority Inversion, Solutions for Heavily Contended Locks, Non-blocking Algorithms, ABA Problem, Cache Line Ping-ponging, Memory Reclamation Problem, Recommendations, Thread-safe Functions and Libraries, Memory Issues, Bandwidth, Working in the Cache, Memory Contention, Cache-related Issues, False Sharing, Memory Consistency, Current IA-32 Architecture, Itanium Architecture, High-level Languages, Avoiding Pipeline Stalls on IA-32,Data Organization for High Performance. Textbook 1: Ch.7 RBT: L1, L2, L3 Course Outcomes: The student will be able to : • Identify the limitations of ILP and the need for multicore architectures • Define fundamental concepts of parallel programming and its design issues • Solve the issues related to multiprocessing and suggest solutions • Make out the salient features of different multicore architectures and how they exploit parallelism • Demonstrate the role of OpenMP and programming concept Question Paper Pattern: • The question paper will have ten questions. • Each full question will have sub questions covering all the topics under a m					
Itextbook 1: Ch.6 RBT: L1, L2, L3 Module-5 Solutions to Common Parallel Programming Problems : Too Many Threads, Data Races, 08 Deadlocks, and Live Locks, Deadlock, Heavily Contended Locks, Priority Inversion, Solutions for Heavily Contended Locks, Non-blocking Algorithms, ABA Problem, Cache Line Ping-ponging, Memory Reclamation Problem, Recommendations, Thread-safe Functions and Libraries, Memory Issues, Bandwidth, Working in the Cache, Memory Contention, Cache-related Issues, False Sharing, Memory Consistency, Current IA-32 Architecture, Itanium Architecture, High-level Languages, Avoiding Pipeline Stalls on IA-32,Data Organization for High Performance. Textbook 1: Ch.7 RBT: L1, L2, L3 Course Outcomes: The student will be able to : • Identify the limitations of ILP and the need for multicore architectures • Define fundamental concepts of parallel programming and its design issues • Solve the issues related to multiprocessing and suggest solutions • Make out the salient features of different multicore architectures and how they exploit parallelism • Demonstrate the role of OpenMP and programming concept Question Paper Pattern: • The question paper will have ten questions. • Each full question consisting of 20 marks • There will be 2 full questions (with a maximum of four sub questions) from each module. • Each full question will have sub questions covering all the topics under a	OpenMP Environment Variables, Compilation, Debugging, performance				
Module-5 Solutions to Common Parallel Programming Problems : Too Many Threads, Data Races, O8 Deadlocks, and Live Locks, Deadlock, Heavily Contended Locks, Priority Inversion, Solutions for Heavily Contended Locks, Non-blocking Algorithms, ABA Problem, Cache Line Ping-ponging, Memory Reclamation Problem, Recommendations, Thread-safe Functions and Libraries, Memory Issues, Bandwidth, Working in the Cache, Memory Contention, Cache-related Issues, False Sharing, Memory Consistency, Current IA-32 Architecture, Itanium Architecture, High-level Languages, Avoiding Pipeline Stalls on IA-32,Data Organization for High Performance. Textbook 1: Ch.7 RBT: L1, L2, L3 Course Outcomes: The student will be able to : • Identify the limitations of ILP and the need for multicore architectures • Define fundamental concepts of parallel programming and its design issues • Solve the issues related to multiprocessing and suggest solutions • Make out the salient features of different multicore architectures and how they exploit parallelism • Demonstrate the role of OpenMP and programming concept Question Paper Pattern: • The question paper will have ten questions. • Each full Question consisting of 20 marks • There will be 2 full questions (with a maximum of four sub questions) from each module. • The students will have to answer 5 full questions, selecting one full question from each module. • The question paper will have to answer 5 ful	Textbook I: Ch.6 RBT· L1 L2 L3				
Solutions to Common Parallel Programming Problems : Too Many Threads, Data Races, 08 Deadlocks, and Live Locks, Deadlock, Heavily Contended Locks, Priority Inversion, 08 Solutions for Heavily Contended Locks, Non-blocking Algorithms, ABA Problem, Cache 108 Line Ping-ponging, Memory Reclamation Problem, Recommendations, Thread-safe 108 Functions and Libraries, Memory Issues, Bandwidth, Working in the Cache, Memory 108 Contention, Cache-related Issues, False Sharing, Memory Consistency, Current IA-32 104 Architecture, Itanium Architecture, High-level Languages, Avoiding Pipeline Stalls on IA-32, Data Organization for High Performance. 108 Textbook 1: Ch.7 RBT: L1, L2, L3 108 Course Outcomes: The student will be able to : 108 108 • Identify the limitations of ILP and the need for multicore architectures 108 108 • Define fundamental concepts of parallel programming and its design issues 109 108 • Solve the issues related to multiprocessing and suggest solutions 108 108 • Make out the salient features of different multicore architectures and how they exploit parallelism 108 • Demonstrate the role of OpenMP and programming concept 109 108 • The question paper will have ten questions. 109 10	Module-5				
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5. Lyia D Das, The Xoo Microprocessors: 8080 to Pentium, Multicores, Atom and the 8051 Microcontroller: Architecture, Programming and Interfacing" 2nd Edition, Pearson Education	Education				
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