Karnatak Law Society's



Vishwanathrao Deshpande Institute of Technology, Haliyal

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

1ST June 2021

ACTIVITY REPORT

PROCEEDINGS PROCEEDINGS PROCEEDINGS PROCEEDINGS PCB DESIGN USING INDUSTRY STANDARD TOOL- EAGLE EDA SOFTWARE" PCB DESIGN USING INDUSTRY STANDARD TOOL- EAGLE EDA SOFTWARE DR. SUBHAS METI, HOD-ECE, SKSVMACET, LAXMESHWAR EVENT DATE: 15T JUNE 2021 EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS TARGET AUDIENCE: OPEN TO ALL STUDENTS OF ECE / EEE PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com//meetup-join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 376897context=%77b%22Tid %22%3a%229727037c1-d404-475d-b12a-177bfc3022Tic%222%22%22%22%3a%22977037c1-d404-475d-b12a-177bfc3022Tic%222%22%22%3a%22977037c1-d404-475d-b12a-177bfc3022Tic%222%22%22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d EVENT CO- ORDINATOR: CLUB COORDINATOR, DEPARTMENT OF E&CE PROF. DEEPAK SHARMA EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE PROF. DEEPAK SHARMA PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: DR. MAHENDRA M. DIXIT, HOD, DEPARTMENT OF E&CE VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF PROFILE REMOVED.	BRIEF REPORT ON				
PROCEEDINGS EVENT TITLE: PCB DESIGN USING INDUSTRY STANDARD TOOL- EAGLE EDA SOFTWARE RESOURCE PERSON: DR. SUBHAS METI, HOD-ECE, SKSVMACET, LAXMESHWAR EVENT DATE: EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS TARGET AUDIENCE: PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com//meetup-join/19%3aa549e563e38e4f0 8abi1321686369h533%.40thread.tacv2/16224695 376897context=%7b%22Tid %22%3a%22727037c1-d404-475d-b12a-17bfc3022f7e%22%2e%22Oid %22%3a%227727037c1-d404-475d-b12a-17bfc3022f7e%22%2e%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%222%2d %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%222%7d EVENT CO- ORDINATOR: CLUB COORDINATOR, DEPARTMENT OF E&CE MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES ADDRESS BY GUEST SPEAKER (PRIME HIGHLIGHTS): PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: OR. MAHENDRA M. DIXIT, HOD, DEPARTMENT OF E&CE VOTE OF THANKS: TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF FINCI OSED.	"ONLINE WORKSHOP ON PCB DESIGN USING INDUSTRY STANDARD TOOL-				
EVENT TITLE: PCB DESIGN USING INDUSTRY STANDARD TOOL- EAGLE EDA SOFTWARE RESOURCE PERSON: DR. SUBHAS METI, HOD-ECE, SKSVMACET, LAXMESHWAR EVENT DATE: 181 JUNE 2021 EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS TARGET AUDIENCE: PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com/l/meetup- join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%227273737c1-d404-475d-b12a-17bfc3022f7e%22%2c%2c020id %22%3a%227273737c1-d404-475d-b12a-17bfc3022f15e9%222%2c%2c02c0id %22%3a%227273737c1-d404-475d-b12a-17bfc3022f15e9%22%2c%2c02c0id %22%3a%2294246dfd-f17d-458e-86e0-44f57d21f15e9%22%2c%2d02c0id %22%3a%2294246dfd-f17d-458e-86e0-44f57d21f15e9%22%2c%2d02c0id %22%3a%22727037c1-d404-475d-b12a-17bfc3022f15e9%22%2c%2d02c0id %22%3a%2294246dfd-f17d-458e-86e0-44f57d21f15e9%22%2c%2d02c0id %22%3a%2294246dfd-f17d-458e-86e0-44f57d21f15e9%22%2c%2d02c0id %22%3a%2294246dfd-f17d-458e-86e0-44f57d21f15e9%22%2c%2d02c0id %22%3a%22727037c1-d404-475d-b12a-17bfc3022f15e9%22%2c%2d02c0id %22%3a%22727037c1-d404-475d-b12a-17bfc3022f1fe9%22%2c%2d02c0id %22%3a%22772037c1-d404-475d-b12a-17bfc3022f1fe9%22%2c%2d02c0id %22%3a%22772037c1-d404-475d-b12a-17bfc3022f1fe9%22%2c%2c%2c%2c%2c%2c%2c%2c%2c%2c%2c%2c%					
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RESOURCE PERSON: DR. SUBHAS METI, HOD-ECE, SKSVMACET, LAXMESHWAR EVENT DATE: IST JUNE 2021 EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS TARGET AUDIENCE: PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com/l/meetup-join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %222%3a%222727037c1-d404-475d-b12a-17bfc302247e%22%2c%22Oid %222%3a%22294246dfd-f17d-4458e-86e0-44f57d2115a9%22%7d EVENT CO- ORDINATOR: CLUB COORDINATOR, DEPARTMENT OF E&CE MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. DEEPAK SHARMA PROF. DEEPAK SHARMA PROF. DEEPAK SHARMA PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF FINCI OSED		PCB DESIGN USING INDUSTRY STANDARD TOOL- EAGLE EDA			
RESOURCE PERSON: EVENT DATE: 19T JUNE 2021 EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS OPEN TO ALL STUDENTS OF ECE / EEE PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com///meetup-join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%2277037c1-d404-475d-b12a-17bfc3022t7e%22%2c%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d EVENT CO- ORDINATOR: MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE 10 SED 425 (25 STAFF + 400 STUDENTS) BRIEF BIODATA OF	EVENT TITLE:	SOFTWARE			
EVENT DATE: 15T JUNE 2021 EVENT TIME: 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION: 5:30 HOURS TARGET AUDIENCE: OPEN TO ALL STUDENTS OF ECE / EEE PLATFORMS: MICROSOFT TEAMS LINK: https://teams.microsoft.com/l/meetup-join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%22792737c1-d404-475d-192a-17bfc3022fre%22%2c%2c%220id %222%3a%227927037c1-d404-475d-192a-17bfc3022fre%22%2c%220id %222%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d EVENT CO- ORDINATOR: CLUB COORDINATOR, DEPARTMENT OF E&CE MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF EVENT DURATION: PROF. DEEPAK SHARMA DIXIT, HOD, DEPARTMENT OF E&CE 10:00 AM TO 12:00 PM AND 2:00 PM TO 4:30 PM EVENT DURATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF ENCLOSED	RESOURCE PERSON:				
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LINK: https://teams.microsoft.com/l/meetup- join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%22727037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d PROF. DEEPAK SHARMA, EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: VOTE OF THANKS: TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF LINK: https://teams.microsoft.com/l/meetup-join/19%3aa549e563e38e4f0 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%2277037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%222%7d PROF. DEEPAK SHARMA, EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: DR. MAHENDRA M. DIXIT, HOD, DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF FINCLOSED	TARGET AUDIENCE:	OPEN TO ALL STUDENTS OF ECE / EEE			
MEET LINK: 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%2277037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d EVENT CO- ORDINATOR: MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF 8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid %22%3a%2277037c1-d404-475d-b12a-17bfc3022f7e%22%c2c%22Oid %22%3a%2277037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid %22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d PROF. DEEPAK SHARMA, EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF	PLATFORMS:	MICROSOFT TEAMS			
### WEET LINK: ### W22%3a%2277037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid ### W22%3a%2294246dfd-f17d-458e-86e0-44f57d2115a9%22%7d ### EVENT CO- ORDINATOR: ### PROF. DEEPAK SHARMA, EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE ### PROF. DEEPAK SHARMA ### WELCOME ADDRESS: ### INTRODUCTION OF GUEST SPEAKER: ### INTRODUCTION OF GUEST SPEAKER: ### Interpretation of the company		LINK: https://teams.microsoft.com/l/meetup-join/19%3aa549e563e38e4f0			
### WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: ADDRESS BY GUEST SPEAKER (PRIME HIGHLIGHTS): PROF. DEEPAK SHARMA HIGHLIGHTS): #### WELCOME ADDRESS SPEAKER (PRIME HIGHLIGHTS): #### PROF. DEEPAK SHARMA #### WELCOME ADDRESS INTRODUCTION OF GUEST SPEAKER: #### PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: ###################################	MEET I INK.	8ab1321686369b533%40thread.tacv2/16224695 37689?context=%7b%22Tid			
PROF. DEEPAK SHARMA, EVENT & ELECTRONIC CIRCUIT DESIGN CLUB COORDINATOR, DEPARTMENT OF E&CE MASTER OF CEREMONY & WELCOME ADDRESS: INTRODUCTION OF GUEST SPEAKER: PROF. PRASANN D. KULKARNI 1. MEANING OF PCB 2. DEFINITION / ADVANTAGES OF SURFACE MOUNT DEVICES 3. CONCEPT OF MULTILAYER PCBS 4. SCHEMATIC OF RC COUPLED CE AMPLIFIER AS A PRACTICAL EXAMPLE 5. VERIFICATION BY ELECTRIC RULE CHECK DRC AND SCHEMATIC TO BOARD CONVERSION PRESIDENTIAL REMARKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE 425 (25 STAFF + 400 STUDENTS) ENCLOSED	WEET LINK:	%22%3a%22727037c1-d404-475d-b12a-17bfc3022f7e%22%2c%22Oid			
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REMARKS: DR. MAHENDRA M. DIXIT, HOD, DEPARTMENT OF E&CE VOTE OF THANKS: PROF. RAHUL C. M., DEPARTMENT OF E&CE TOTAL NUMBER OF PARTICIPANTS: BRIEF BIODATA OF ENCLOSED	PRESIDENTIAL.				
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TOPICS COVERED: ENCLOSED	TOPICS COVERED:	ENCLOSED			
LIST OF FACULTY ENCLOSED	LIST OF FACULTY	ENCLOSED			
PARTICIPANTS:	PARTICIPANTS:	ENCLUSED			
EVENT SNAPSHOTS: ENCLOSED	EVENT SNAPSHOTS:	ENCLOSED			

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18 Years of Teaching Experience

Education:

Doctor of Philosophy- 2019 - VTU, Belagavi

Achievements:

- Published more than 15 articles in renowned Journals / Conferences
- Reviewer: American Journal of Neural Networks and Applications.
- Reviewer: Springer Nature Journal of Computer Science
- · Reviewer: Advances in Science, Technology and Engineering System Journal

TOPICS COVERED

- 1. MEANING OF PCB
- 2. SURFACE MOUNT DEVICES
- 3. DEFINITION / ADVANTAGES
- 4. MULTILAYER PCBS
- 5. PRACTICAL SESSION SCHEMATIC OF RC COUPLED CE AMPLIFIER
 - IDENTIFICATION OF DEVICES OUT OF FAMILY OF COMPONENTS
 - DEVICE SELECTION FROM LIBRARY
 - ELECTRIC RULE CHECK DRC
 - SCHEMATIC TO BOARD CONVERSION
 - AUTOMATIC PLACE & ROUTE
 - DRC FOR PARAMETERS MAGNITUDE MEASUREMENTS AND DECIDING TRACK SIZE
- 6. PRACTICAL SESSION SCHEMATIC OF MULTIVIBRATOR USING 555 TIMER
 - IDENTIFICATION OF DEVICES OUT OF FAMILY OF COMPONENTS
 - DEVICE SELECTION FROM LIBRARY
 - ELECTRIC RULE CHECK DRC
 - SCHEMATIC TO BOARD CONVERSION
 - AUTOMATIC PLACE & ROUTE
 - DRC FOR PARAMETERS MAGNITUDE MEASUREMENTS AND DECIDING TRACK SIZE

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7	PROF. BASAVARAJ G.	KLS VDIT HALIYAL	
8	PROF. VINAY C.	KLS VDIT HALIYAL	
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12	PROF. PLASIN DIAS	KLS VDIT HALIYAL	

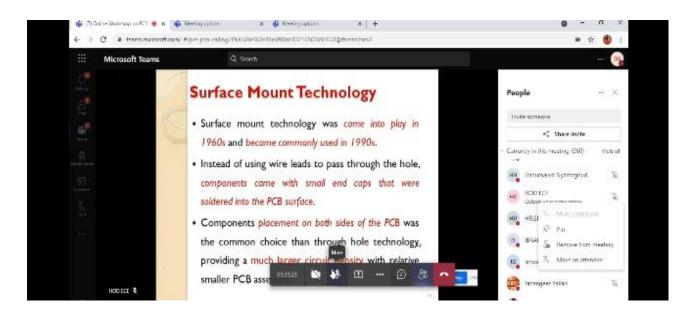
13	PROF. VARAPRASAD G	KLS VDIT HALIYAL
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15	PROF. VIKAS BALIKAI	KLS VDIT HALIYAL
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23	PROF. RAJESHWARI NANANNAVAR	KLS VDIT HALIYAL
24	DR. UTTAM SATPUTE	KLS VDIT HALIYAL
25	MR. MANJUNATH MOVLEKAR	KLS VDIT HALIYAL

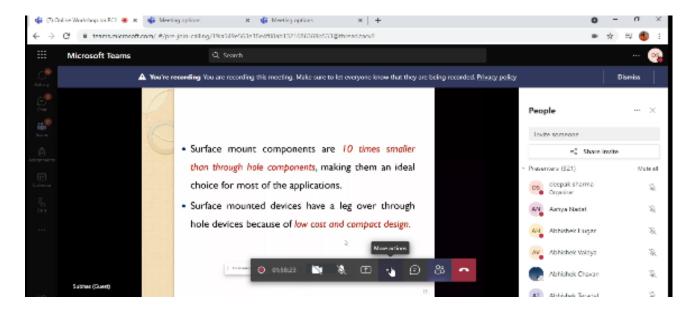
GALLERY – EVENT SNAPSHOTS PICTURE 1: EVENT FLYER



PICTURE 2: LIVE MEET OF THE WORKSHOP ON MS TEAMS







PICTURE 3: EVENT E-CERTIFICATE





Vishwanathrao Deshpande Institute of Technology , Haliyal



CERTIFICATE OF PARTICIPATION

This is to certify that,

Mr. /Ms. <u>Deepak Sharma</u> bearing USN <u>123</u> from <u>8</u> Semester of <u>ECE</u> Department, KLS VDIT HALIYAL

has attended One Day Online Workshop on

"PCB Design using Industry Standard Tool - EAGLE EDA Software"

Organized by

Department of E&CE and E&EE in association with Electronic Circuit Design Club of Innovation Forum, KLS VDIT Haliyal on June 01, 2021

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Prof. A.V.Kolaki

Dr. Subhas Meti

Dr. Mahendra M. Dixit

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