

Karnatak Law Society's Vishwanathrao Deshpande Institute of Technology, Haliyal (Approved by AICTE, New Delhl, Amilated to VTU, Belagavi) (Recognized Under Section 2(f) by UGC, New Delhi) Udyog Vidya Nagar, Haliyal – 581329, Dist: Uttar Kannada Phone: 08284-220861, 220334, 221409, Fax: 08284-220813 Web: www.klsvdit.edu.in Email: principal@klsvdit.edu.in

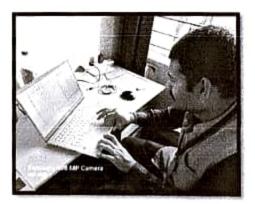


## Department of Electronics and Communication Engineering

## MOU Activity Report

## Crevavi Technologies, Germany

Particulars	Details
Activity Name:	Research about Semiconductor materials (Si, SiC and GaN) for the application in design of Inverter for Electric Vehicles
Participants	Dr. Mahendra M. Dixit, Prof. A. S. Joshi Prof. Deepak Sharma
Faculty Coordinator	Dr. Mahendra M. Dixit
Purpose	To bring out the differences in various Semiconductor materials (Si, SiC and GaN)for the application in design of Inverter for Electric Vehicles
Expected Outcome	Requirements and expectations of customer (Pros and Cons of Power Modules related to efficiency, Switching Frequency, Size, Losses, Cooling and Impact on associated component) to be met as per the need and specifications in Electric Vehicle design
Duration of Event	9 <sup>th</sup> April 2021 to 9 <sup>th</sup> May 2021
Date of Report Submission	18 <sup>th</sup> MAY 2021
Number of Participants	3
Venue	Research Lab, Dept. of E&CE, KLS VDIT, Haliyal.



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Head of the Department Cept of Electronic & Communication Energy KLS V.D.LT. The IVAL (2013)