



KLS'S

Vishwanathrao Deshpande Institute of Technology



DEPARTMENT OF ELECTRONICS AND COMMUNICATION

Webinar on “Semiconductor Industry: Opportunities and Career Prospects” by Mr. Ravi Siddanath, Principal Design Engineer, Broadcom, Bengaluru on the date of 11-07-2020.

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology
Haliyal
Training & Placement Cell

Online Webinar on
"Semiconductor Industry : Opportunities & Career Prospects"
for the students of ECE/EEE branch of KLS VDIT

Profile of the Resource person
Mr. Ravi has served at various levels.
Karnataka Microelectronics (KMEI)
Member of Technical Staff (July 2007 - Dec 2010)
IC Characterization, Layout, Masking Layout
ICM, Circuit Designer (Jan 2011-June 2013)
Memory Compiler Layout
Broadcom, Principal Design Engineer
July 2013 - till date)
Memory Compiler Layout and Automation
Team, Test, Sdk, Factory

Resource Person:
Ravi Siddanath
Principal Design Engineer
Broadcom, Bengaluru

Registration link
<https://forms.gle/SPnCkQ5g6XkXwpZ7>

Only registered students will be sent invitation for the webinar

Date: 11-07-2020 (Saturday)
Time : 9:15 AM to 10:30 AM

Coordinator: Prof. Vikas Balikai (Director T & P)
Mr. Umesh Avasthakar - TPO
Prof. Deepak Sharma - ECE Dept

Webinar on ECE dept. facilities and admission procedure was delivered by Dr. Arun Kakhandki, HoD, ECE Dept., and moderated by Prof. Vikas Balikai for PU students under KLS Academy, Belagavi on the date of 7-08-2020.

KLS
Vishwanathrao Deshpande Institute of Technology
Haliyal

In association with

Organizing webinar for
All Engineering Aspirants of 2020

Friday, August 7th 2020
10:30 AM

Join Meeting
GoToMeeting
ID:741996141

Dr. V.V. Katti
Principal

Dr. V.A. Kulkarni
Vice Principal

Webinar on ECE dept. facilities and admission procedure was delivered by Dr. Arun Kakhandki, HoD, ECE Dept., and moderated by Prof. Vikas Balikai for PU students from Hubballi, Dharwad, Sirsi, Kumta, Ankola, Dandeli, Haliyal and many more on the date of 11-08-2020

Karnatak Law Society's
VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY
Haliyal (Under Institute of Engg. Regulation)

A Four Days Webinar
Why Engineering at KLS VDIT

THE VALUE OF ENGINEERING EDUCATION
CAN BE UNDERSTOOD ONLY BY
EXPERIENCE IT

Join the Marathon Webinar from
AUG 11, 2020 to AUG 14, 2020

Registration Link (optional)
<https://forms.gle/5m4qgms>

WEBINAR LINK
<https://global.gotomeeting.com/join/52146949>

If you are unable to join the webinar by the above link,
then join this webinar via
<https://youtu.be/788978m38EC>

This webinar is a unique chance to...
know more about Engineering education,
know about the career path and its
importance why you should join KLS VDIT, Haliyal

Registration Session (10:00 to 11:00)
Aug 11 About KLS VDIT
Aug 12 Basic Science
Aug 13 Mechanical Engg
Aug 14 Civil Engineering

Registration Session (2:30 to 3:00)
Aug 11 Training & Placement
Aug 12 Electronics & Communication Engg
Aug 13 Computer Systems & Engg
Aug 14 Electrical & Electronics Engg

Our Recruiters
BOSCH
Infocys

Dr. V. V. Katti
Vice Principal

Dr. V. V. Katti
Principal

Webinar on “Right time and right strategy to prepare for GATE Exam” was presented by alumni Mr. Siddesh Gaonkar. Coordinator/s: Prof. Plasin Dias and Prof. Vinay Chitare on the date of 08-08-2020

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology,
Haliyal

Department of Electronics and Communication Engineering

Webinar on How to Prepare
GATE 2021

What is the
Right Time & Right Strategy
to Prepare for
GATE

Live on
Free Registration
Google Meet

WHEN TO CONNECT: DATE 08 AUG 2020
Time 10:30-11:30 AM

Coordinator: Prof. Plasin Dias
HOD: Dr. Arun Kakhandki
Vice Principal: Dr. V.A. Kulkarni
Principal: Dr. V.V. Katti

Webinar on “Opportunities in VLSI & embedded systems” by Mr. Kubendra, Senior Manager, Samsung R&D, Bengaluru. No. of participants: 90 students from neighboring engineering colleges and KLS VDIT. on the date of 18-08-2020

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology

Department of Electronics & Communication Engineering
Training & Placement Cell

organizes an
Industry expert talk (Webinar) on
OPPORTUNITIES IN VLSI DESIGN & EMBEDDED SYSTEMS

Target Audience 2nd, 3rd & 4th Year Students of ECE & EEE Branches

Join us live through gate meeting with this link
<https://global.gotomeeting.com/join/52146949>
or open gate meeting app & enter the
Meeting ID: 52146949

Date: 18-08-2020, 11 AM

Mr. Kubendra Kumar
Senior Manager
Samsung Semiconductor R & D, Bengaluru

Coordinated by
Prof. Vikas Balikai
Director T & P
KLS VDIT

Dr. Arun Kakhandki
HOD

Dr. V. A. Kulkarni
Vice Principal

Dr. V. V. Katti
Principal

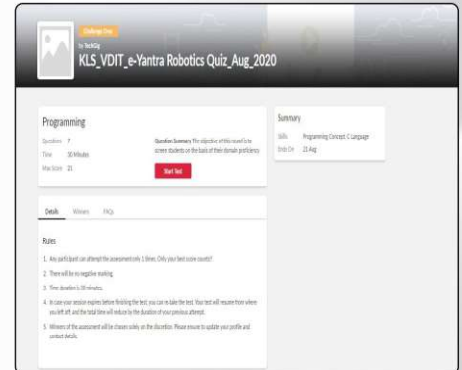


DEPARTMENT OF ELECTRONICS AND COMMUNICATION

e-Yantra Robotics Quiz was conducted for the students of 5thsem ECE, EEE, CS and Mech. For short listing for e-Yantra Robotics competition 2020-21.

No. of participants: 80

Coordinator/s: Prof. Vikas Balikai & Prof. Rahul C. M.
on the date of 21-8-2020.



WEBINAR ORGANISED

A Webinar on “Recent Trends and Opportunities in Artificial Intelligence (AI)” was organized for PG students of ECE department and Mech. Department on 5th September 2020. Resource person, Mr. Gautam Shigaonkar CEO, HAEGE TECHNOLOGIES PVT. LTD., Dharwad/ Bangalore on 05/09/2020.

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology
Haliyal

ISTE Student Chapter Presents

**A WEBINAR ON
"Recent Trends and
Opportunities in AI"**

Organized by
Dept. of ECE in association with FIJI & ED Cell

RESOURCE PERSON
SHRI. GAUTAM SHIGAONKAR
CEO, HAEGE TECHNOLOGIES PVT LTD
BANGALORE

eCertificates will be provided
Free Registration
Date-05/09/2020
Time-11am

Target Audience-PG Students
<https://meet.google.com/xzn-jiik-akz>

A.S.JOSHI
HEMANT
EVENT COORDINATOR

DR.S.P.HANDE
JYOTSNA
JTS COORDINATOR

ANUPAMA A
DEPT. ISTE COORDINATOR

DR.RAMKRISHNA JOSHI
DEAN FIJI & ED CELL

Dr. Arun L. K.
HOD

Dr. V. A. Kulkarni
Vice-Principal

Dr. V. V. Katti
Principal

Webinar on “A JOURNEY FROM ENGINEER TO ENTREPRENEUR” was organized by Prof. Prasann D. Kulkarni, and conducted by Resource Person, Prakash Mugali, Founder and MD, Enerzi microwave systems Belagavi on 11/09/2020.

Karnatak Law Society's
Vishwanathrao Deshpande Institute
of Technology. Haliyal

**WEBINAR ON
A JOURNEY FROM
ENGINEER TO
ENTREPRENEUR**

By the Renowned
Personality
Dr. Prakash Mugali,
Founder Chairman & MD,
ENERZI MICROWAVE
SYSTEMS , Belagavi

On 11th
September
at 3.30PM

Registration Link:
<https://forms.gle/7VagGRcwFA4rEHGd9>

QR Code for
Registration

Program Coordinator
Prof. Prasann D. Kulkarni
Prof. Ravi Motekar
Prof. Bharatkumar Patil

Vice Principal
Dr. V. A. Kulkarni

Principal
Dr. V. V. Katti

ALUMNI TALK

Alumni Talk Series 2 on “CAREER PLANNING DURING AND AFTER ENGINEERING” Resource Person Mr. Rahul Kulkarni, Senior Consultant, Deloitte, Bengaluru on 26/09/2020.

Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics and Communication Engineering

Alumni Talk Series #2

Webinar on
Career Planning During and After Engineering

Resource Person
Mr. Rahul Kulkarni
Senior Consultant
Deloitte, Bengaluru
Our Proud Alumni
2009 Batch

Target audience
3rd, 5th, 7th Sem

Free Registration

WHEN TO CONNECT: Date 26 Sep 2020
Time 11-12 AM (including audience Q&A)

Live on
Google Meet

Prof. Plasin Dias
Prof. Vinay Chitare
Alumni Coordinator

Dr. Arun Kakhandki
HOD

Dr. V.A. Kulkarni
Vice Principal

Dr. V.V. Katti
Principal

ADD-ON COURSES

Preparation and finalization of Poster for the Value-Added on 22-09-2020 to 24-09-2020.

FOR THIRD SEMESTER STUDENTS :

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics & Communication Engineering

VALUE ADDED TRAINING PROGRAM ON
ADVANCED MICROPROCESSOR

Training Content	Training Objectives	Training Outcomes	Training
<ul style="list-style-type: none"> -Architecture & addressing modes -Data movement, arithmetic & logical instructions -Program control instructions & Assembly Programming -Hardware Interfacing 	<ul style="list-style-type: none"> -Familiarize basic architecture of 8086 microprocessor -Program 8086 Microprocessor using Assembly Level Language -Use Macros and Procedures in 8086 Programs -To design an interface experiment to communicate processor with external world. 	<ul style="list-style-type: none"> -Explain the History of evolution of general-purpose processors, Architecture and instruction set of 8086, CPU Architecture, Configuration & Timing diagrams of 8086 -Write 8086 Assembly level programs using the 8086-instruction set. -Able to interface stepper motor with processor using 8255. 	<ul style="list-style-type: none"> Duration: 30 Hours Theory: 18 Hours Hands On: 12 Hours Target Audience: 3rd Semester

TRAINERS

Prof. Rohini K Asst. Prof. Dept. of ECE	Prof. Shree Gowri Asst. Prof. Dept. of ECE	Prof. Meenaxi T Asst. Prof. Dept. of ECE
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Dr. Arun Kakhandki
HoD

Dr. V.A. Kulkarni
Vice-Principal

Dr. V.V. Katti
Principal

FOR FIFTH SEMESTER STUDENTS :

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology, Haliyal
 Department of Electronics & Communication Engineering

VALUE ADDED TRAINING PROGRAM ON
ALGORITHMIC APPROACH TO SOLVE A PROBLEM USING C

<p>Training Content</p> <ul style="list-style-type: none"> -Example problem definitions & problem analysis w.r.t variables involved & their types -Algorithmic approach to solve the above example problems -Programs using C for the above example problems & program generalization -Execution of the above programs 	<p>Training Objectives</p> <ul style="list-style-type: none"> -Analyze the problems in terms of variables involved & their types in particular sorting techniques -Algorithmic approach to solve the sorting problems along with flow charts -Coding the algorithmic steps in C & performance analysis of sorting techniques 	<p>Training Outcomes</p> <ul style="list-style-type: none"> -To acquire the knowledge of analysing the problems based on sorting techniques. -To estimate the algorithmic approach & draw the flow charts -To write the programs in C & to analyse the performance of sorting techniques
<p>TRAINER Prof. Basavaraj Goudar Asst. Prof. Dept. of ECE</p>		
<p>Training Duration: 30 Hours Theory: 18 Hours Hands On: 12 Hours Target Audience: 5th Semester</p>		

Dr. Arun Kakhandki
HoD
Dr.V.A.Kulkarni
Vice-Principal
Dr.V.V.Katti
Principal

FOR THIRD AND FIFTH SEMESTER STUDENTS :

Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology
 Haliyal
 Department of Electronics & Communication Engineering

VALUE ADDED TRAINING PROGRAM ON
EMBEDDED ROBOTICS - a task based training
 In association with E-Yantra Lab – KLS VDJIT

<p>Training Content</p> <ul style="list-style-type: none"> -Embedded System -Robotics Fundamentals -Buzzer, Display & timer concepts -Firebird V Architecture -Robot motion -White line follower 	<p>Training Outcomes</p> <ul style="list-style-type: none"> - In Depth knowledge of Embedded system concepts -Participation in e-Yantra Robotics Competition (eYRC) hosted by IIT Bombay -Students can work on these concepts as their final year projects. 	<p>Training Objectives</p> <ul style="list-style-type: none"> -Task Based Training (TBT) is an endeavour to train students with Firebird V robot to implement hands-on experiments related to embedded systems with each task aimed at learning a specific concepts. 	<p>Training Duration: 30 Hours Theory: 10 Hours Hands On: 20 Hours</p> <p>Target Audience: 5th Semester and 3rd Semester Students of ECE branch</p>
<p>TRAINERS</p>			
<p>Prof. Vikas Balikai Asst Prof. Dept of ECE & Director-Training & Placement</p>		<p>Prof. Rahul C. M Asst Prof. Dept of ECE</p>	

Dr. Ramkrishna Joshi
Coordinator - Eyantra Lab
Dr. Arun Kakhandki
HoD - Dept of ECE
Dr. V. A. Kulkarni
Vice-Principal
Dr. V. V. Katti
Principal

MOCK DCET

Poster preparation for Mock DCET for near by E&CE department diploma college students.

**Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology, Haliyal**

Department of Electronics & Communication Engineering
Invites all aspirants to

**ONLINE MOCK DCET - 2020
(FOR DIPLOMA ECE STUDENTS)**

On 05th October at
11.30AM

CET CODE-E135
COMED-K CODE-E74

QR Code for Registration

For more details, contact:
Prof. Rahul C.M. +91-8277536692
Prof. Deepak Sharma +91-9481860756

Registration Link:
<https://forms.gle/GyNvxGarQ4pAsK2h8>

For College Information log onto
www.klsvdit.edu.in

Program Co-ordinators:
Prof. Prasann D. Kulkarni Prof. Vinay Chitare Prof. Raghavendra Nagaralli Prof. Nikhil Kulkarni

HoD, ECE
Dr. Arun Kakhandki

Vice Principal
Dr. V. A. Kulkarni

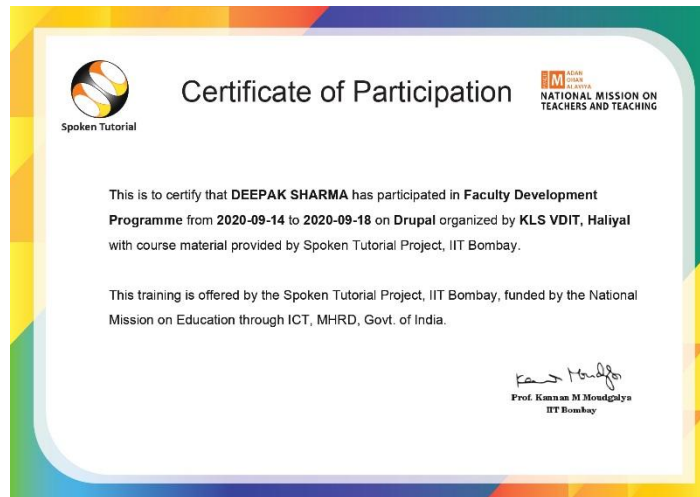
Principal
Dr. V. V. Katti

FDP ATTENDED

Prof. Deepak Sharma of KLS VEDIT Haliyal has participated in the Five Days Online Faculty Development Programme on “Machine Learning and Data Science”, organized by the Department of Computer Science and Engineering in Technical Association with IEEE Bangalore Section, from 31/08/2020 to 04/09/2020.



Prof. Deepak Sharma has participated in Faculty Development Programme from 14/09/2020 to 18/09/2020 on Drupal organized by KLS Vdit, Haliyal with course material provided by Spoken Tutorial Project, IIT Bombay.



Prof. Deepak Sharma Assistant Professor of KLS Vdit, Haliyal, (U.K.) participated & completed successfully AICTE Training and Learning (ATAL) Academy Online FDP on "Sensors Technology" from 21/9/2020 to 25/9/2020 at National Institute of Engineering.



Prof. Rahul C.M Assistant Professor of KLS Vdit, Haliyal, (U.K.) participated & completed successfully two weeks online FDP on "Demystifying 5G RF ASICs" from 24/8/2020 to 04/9/2020 at Electronics & ICT academy Meity Govt of India.



Prof. Vinay Chitare Assistant Professor of KLS VEDIT, Haliyal, (U.K.) participated & completed successfully two weeks online FDP on "Demystifying 5G RF ASICs" from 24/8/2020 to 04/9/2020 at Electronics & ICT academy Meity Govt of India.



Faculty achievements

Prof Anupama Sanjay Awati has successfully completed her viva voce for the research work entitled “Digital Image inpainting techniques” on September 07 2020 as a partial fulfilment for Ph.D. VTU Belagavi



Congratulations!



Mrs. Anupama Sanjay Awati

on completing her

Ph.D.

ALUMNI TALK

Department of Electronics and Communication Engineering has organized webinar on 17th Oct, 2020 on “Introduction to IT and Embedded Industry” as a part of Alumni Talk Series in association with IEI Students Chapter, Department of ECE, VDIIT, Haliyal .

Mr. Jeevan Gouda S Patil, Senior Technical Specialist, TPVision, India Private Limited, Bengaluru was the resource person. He delivered talk on opportunities available to all the upcoming engineers In IT and Embedded industry with emphasis on different technical skills required to enter at different levels of industries. Also suggested that everyone should have career planning based on individual interests and acquired skills.

Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics and Communication Engineering
Alumni Talk Series #3

Webinar on
Introduction to IT and Embedded Industry
In Association with IEI Students' Chapter

Resource Person
Mr. Jeevangouda.S. Patil
Senior Technical Specialist
TPVision India Pvt.
LTD, Bengaluru
Our Proud Alumni
2005-2009 Batch

Embedded Systems Career-An Outline
FIND OPPORTUNITIES

Target audience
3rd & 5th Sem

Free Registration

WHEN TO CONNECT: Date 17 oct 2020
Time 11 AM-12 PM (including audience Q&A)

Live on
Google Meet

Prof Plasin Dias
Prof Vinay Chitare
Alumni Coordinator

Dr. Arun Kakhandki
HOD

Dr. V.A. Kulkarni
Vice Principal

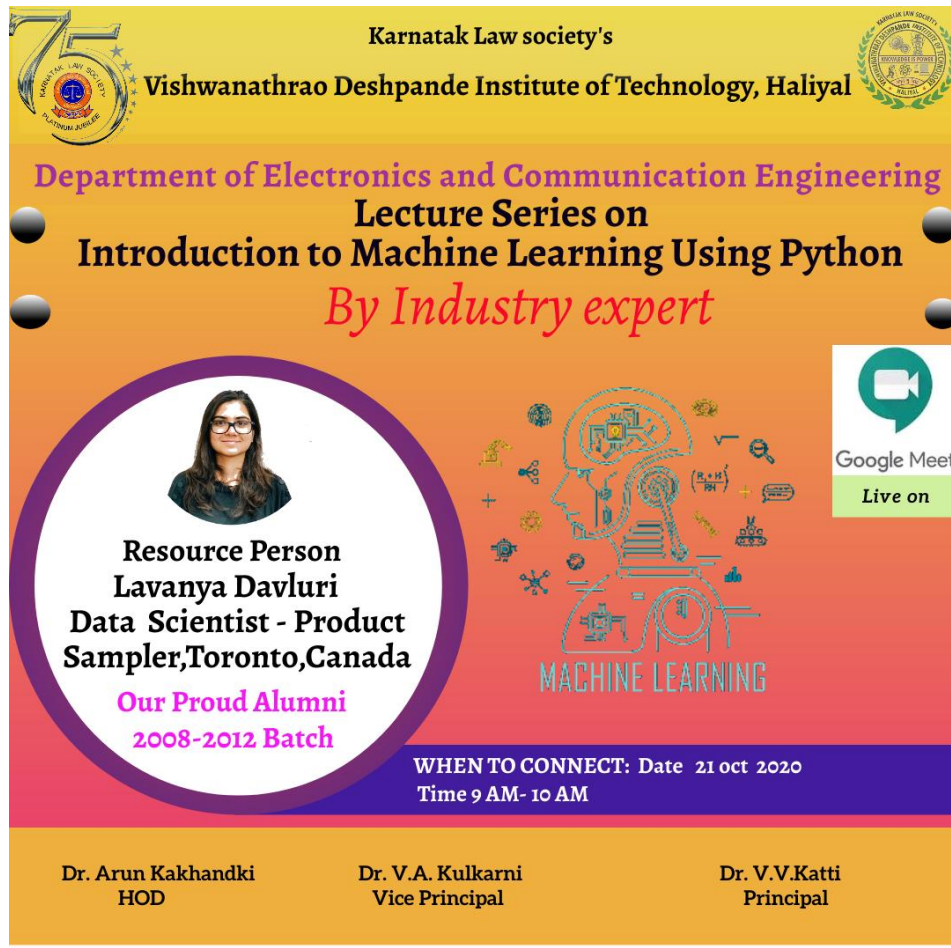
Dr. V.V. Katti
Principal

ADD-ON COURSES

On 01-10-2020, Mr. Anil Mugali Senior R&D engineer, Enerzi Systems, Belagavi, addressed students on topic “**Industrial PCB design and fabrication steps**”, This event was co-ordinated by Prof. Prasann D Kulkarni , ECE Dept, Prof. Deepak Sharma , ECE Dept, Prof. Rahul C.M , ECE Dept. This Add on course was intended to highlight the students with the present opportunities to become Embedded designer and the Need of simulation in real time industry .The Inauguration has been live telecasted and has been viewed by more than 1000 stake holders of the institute.


	<p>KLS Vishwanathrao Deshpande Institute of Technology Haliyal Department of E&CE Value Added Training Program on PCB Design and Circuit Simulation</p>	
<p>Training Content:</p> <ul style="list-style-type: none">• Analog Circuit Design using Express PCB.• Digital Circuit Design using Multisim	<p>In association with Electronic Circuit Design Club, Innovation Forum</p>	<p>Training Outcomes:</p> <ul style="list-style-type: none">• In depth knowledge of concepts for Analog & Digital System Design• Hobby Projects
		
<p>Training Duration 30 Hours</p>	<p>Objectives:</p> <ul style="list-style-type: none">• Learning the Software for circuit Designing• Basic Knowledge of Designing the Circuits• Encourage students to do Hobby Projects	<p>Target Audience: 3rd & 5th sem students</p>
<p>Prof. Deepak Sharma Coordinator - Electronic Circuit Design Club</p>	<p>Trainers : Prof. Prasann D. Kulkarni (Dept. of E&CE) Prof. Deepak Sharma (Dept. of E&CE)</p>	<p>Dr. V. V. Katti Principal</p>
	<p>Dr. Arun Kakhandki HoD - Dept of ECE</p>	<p>Dr. V. A. Kulkarni Vice Principal</p>

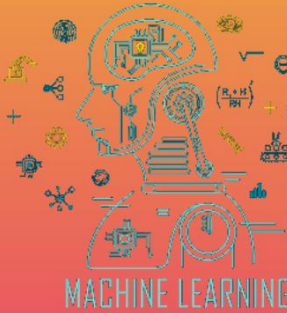
On 21-10-2020, Miss Lavanya Davluri addressed students on topic “**Industrial machine learning using python**”, **Data Scientist-Product Sampler, Toronto-Canada**. This event was co-ordinated by Prof. Vinay Chitare , ECE Dept, Prof. Raghavendra Nagaralli , ECE Dept, Prof. Nikhil Kulkarni , ECE Dept. This Add on course was intended to highlight the students with the present developments in the field of machine learning .

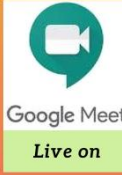


Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics and Communication Engineering
**Lecture Series on
Introduction to Machine Learning Using Python**
By Industry expert


**Resource Person
Lavanya Davluri
Data Scientist - Product
Sampler, Toronto, Canada**
**Our Proud Alumni
2008-2012 Batch**


MACHINE LEARNING


Google Meet
Live on

**WHEN TO CONNECT: Date 21 oct 2020
Time 9 AM- 10 AM**

Dr. Arun Kakhandki
HOD

Dr. V.A. Kulkarni
Vice Principal


Dr. V.V.Katti
Principal

FDP ATTENDED


Prof. Raghavendra Prakash Nagaralli of KLS VBIT Haliyal has completed “**Using Python to Access Web Data**” an online non-credit course authorized by University of Michigan and offered through Coursera.



Webinars Organized



Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology
Haliyal




Department of Electronics & Communication Engg
In association with Training & Placement Cell
Organizes a webinar on


How to ace Interviews
for 7th Semester ECE Students

By
Vibrant Publishers, Bengaluru
Date: 31-10-2020
Time: 10:30 am

Coordinator Prof. Deepak Sharma	T&P Director Prof. Vikas Balikai	HoD Dr. Arun Kakhandki	Principal Dr. V. V.Katti
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Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology
Haliyal



Department of Electronics & Communication Engg
In association with Training & Placement Cell
Organizes a webinar on

English vocabulary building for undergraduates
for 5th Semester ECE Students

By
Vibrant Publishers, Bengaluru
Date: 30-10-2020
Time: 12:00 pm

Coordinator Prof. Deepak Sharma	T&P Director Prof. Vikas Balikai	HoD Dr. Arun Kakhandki	Principal Dr. V. V.Katti
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Activities @ ECE Dept. during November 2020

SL.NO	LIST OF ACTIVITIES CARRIED OUT	
1	<p>Electronics & Communication Engineers association (ECO) and IEI students' Chapter, has organized webinar on “Current Technology Trends in IT and Interview Tips” under Alumni Talk Series on 06/11/2020 by Mr. Gururaj Patil, Senior Software Engineer, Citrix, Bengaluru.</p>	
2	<p>ADD On course on “Introduction to Machine Learning using Python” by our alumni Ms. Lavanya Davluri and she has conducted 6 expert talk series from on 21/11/2020.</p>	
3	<p>E&C dept. has initiated Best Practices for dept. faculties.</p> <p>Event Name: Best Practices Series - 1</p> <p>About the Event: The session gives the insight about the Recent Trends and Advancements in the Machine Learning, Various Supervised Algorithms were presented and interaction was made about their implication and implementation for the real-time scenarios.</p> <p>Topic: Basics of Machine Learning and its Applications</p> <p>Resource Person: Dr. Ramakrishna Joshi Date: 21st November 2020</p>	

E&C dept. has initiated **Best Practices** for dept. faculties.

Event Name: Best Practices Series 2

About the Event: Series-2 gives the insight about supervised algorithms (Logistic regression) along with data sets.

4

Topic: ***Basics of Machine learning and its Applications***

Resource Person:

Dr. Ramkrishna Joshi

Date: 28th November 2020



ALUMNI TALK

Electronics and communication Engineers association (ECO) and IEI students' Chapter, ECE Department KLS's Vdit, under ALUMNI TALK SERIES has organized Webinar on **"Industrial Approach to Wireless Communication"**. The Resource Person was Our Proud Alumni (2008-2011 Batch), Mr. **Mr. Jayaram Shanbhag Senior Chief Engineer Advanced modem group Samsung Research India, Bangalore**. Target Audience: 5th & 7th Semester Students, Date: 19-12-2020, Time: 11Am -12Pm, Platform: Google Meet: <https://meet.google.com/dsq-buct-sqe>, Registration Link: <https://forms.gle/uRSgWDKdfskxY7G5A>. This Session was moderated by: Prof Plasin Dias & Prof Vinay Chitare Alumni Coordinators, Dept. of ECE Engineering, KLS's Vdit Haliyal.



Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics and Communication Engineering

Alumni Talk Series #5

Webinar on
Industrial Approach to Wireless Communication
In Association with IEI Students' Chapter

Resource Person
Mr. Jayaram Shanbhag
Senior Chief Engineer
Advanced modem group
Samsung Research India,
Bangalore
Our Proud Alumni
2008-2011 Batch

Target audience
5th & 7th Sem

Free Registration

WHEN TO CONNECT: Date 19 Dec 2020
Time 11 AM-12 PM (including audience Q&A)

Live on
Google Meet

Prof Plasin Dias
Prof Vinay Chitare
Alumni Coordinator

Dr. Arun Kakhandki
HOD


Dr. V.A. Kulkarni
Principal


ADD-ON COURSES

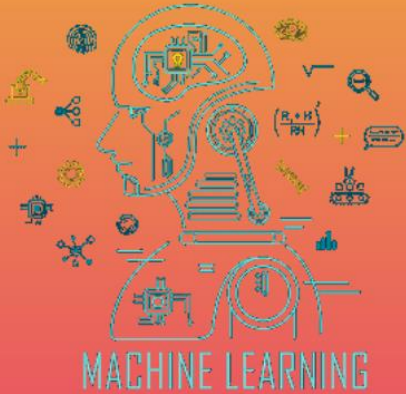
1. On 19-12-2020 and 23-12-2020, Miss Lavanya Davluri, **Data Scientist-at Product Sampler, Toronto- Canada** addressed students on the topic “**Introduction to machine learning using python**”. This event was co-ordinated by Prof. Vinay Chitare, ECE Dept, Prof. Raghavendra Nagaralli, ECE Dept, Prof. Nikhil Kulkarni, ECE Dept. This Add on course was intended to highlight the students with the present developments in the field of machine learning.

Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal

Department of Electronics and Communication Engineering
Lecture Series on
Introduction to Machine Learning Using Python
By Industry expert


Resource Person
Lavanya Davluri
Data Scientist - Product
Sampler, Toronto, Canada
Our Proud Alumni
2008-2012 Batch


Google Meet
Live on


MACHINE LEARNING

Dr. Arun Kakhandki
HOD

Dr. V.A. Kulkarni
Principal

2. Prof Prasann Kulkarni, Prof Deepak Sharma and Prof.Rahul C.M of E&CE dept addressed students on the topic “**PCB design and Circuit Simulation**”. This Add on course was intended to highlight the students with the present developments in the field of PCB Design, The schedules are as mentioned below

SL NO	DATE	SESSION ENGAGED BY	DURATION IN (HRS)	PLATFORM USED
1	7/12/2020	RAHUL C M	1	Microsoft Teams
2	10/12/2020	PRASANN KULKARNI	1	Microsoft Teams
3	14/12/2020	PRASANN KULKARNI	1	Microsoft Teams
4	15/12/2020	DEEPAK SHARMA	1	Microsoft Teams
5	16/12/2020	DEEPAK SHARMA	1	Microsoft Teams
6	17/12/2020	RAHUL C M	2.30	Microsoft Teams
7	19/12/2020	RAHUL C M	1	Microsoft Teams
8	21/12/2020	PRASANN KULKARNI	1	Microsoft Teams
9	24/12/2020	DEEPAK SHARMA	1	GOOGLE MEET
10	31/12/2020	DEEPAK SHARMA	1	Microsoft Teams

FDP ATTENDED

Prof. Rohini Kallur of E&CE Department has participated in

1. Attended webinar on E-Resources Organized by department of Library Science and Information Centre on 31/12/2020
2. Participated in TEQIP-III sponsored one day online workshop on Awareness on Cyber Security-Industry perspective in association with Hewllet Packward Enterprises held on 22nd Dec 2020 at BMSCE Bangalore
3. Attended 3 days webinar on Artificial Intelligence and Machine Learning and its applications organized by Dept of ISE, BIT Bangalore on 1st, 2nd and 4th Dec 2020

Prof. Shreegouri S.S of E&CE Department has participated in

1. Attended webinar on E-Resources Organized by department of Library Science and Information Center on 31/12/2020
2. Participated in TEQIP-III sponsored one day online workshop on Awareness on Cyber Security-Industry perspective in association with Hewllet Packward Enterprises held on 22nd Dec 2020 at BMSCE Bangalore
3. Attended 3 days webinar on Artificial Intelligence and Machine Learning and its applications organized by Dept of ISE, BIT Bangalore on 1st, 2nd and 4th Dec 2020

Prof. Basavaraj D.G of E&CE Department has participated in

1. Attended 3 days webinar on Artificial Intelligence and Machine Learning and its applications organized by Dept of ISE, BIT Bangalore on 1st, 2nd and 4th Dec 2020

Prof. Vikas Balikai of E&CE Department has participated in

1. AICTE-AQIS sponsored Short Term Online Training Program under "Enabling Wireless Technology: Software Defined Radio Applications on Communications using USRP RIO" by School of ECE, KLE Technological University, Hubballi from 07/12/2020 to 12/12/2020
2. AICTE sponsored Virtual Short Term Online Training Program on " Awareness of Intellectual property Rights for Engineers & Industrialists" by S.G.B.I.T Belagavi from 07/12/2020 to 12/12/2020

Webinar organized

KARNATAK LAW SOCIETY'S
Vishwanathrao Deshpande Institute of Technology
Haliyal



Department of ECE presents a
Industrial Talk on

"UPDATES IN INDUSTRY-5G"



Eminent Speaker

Shri. Sandeep Kulkarni

Software Manager
Maxlinear Technologies Pvt. Ltd
Bangalore

Date-21/12/2020

Time:11-12PM

Target Audience-UG/PG Students

<http://meet.google.com/jyh-juoj-pix>



Event Coordinators

Prof.A. S. Joshi
Prof.Meenaxi T

HoD

Dr.Arun L. K.

Principal

Dr.V. A. Kulkarni

A one day technical webinar on updates in industry:5G was organised on 21st December 2020 for PG (Industrial Electronics) and UG students of Electronics and Communication Engg. Department.

Speaker Details

Name:

Mr. Sandip Kulkarni

Software Manager,

Maxlinear Technologies Pvt. Ltd., Bangaluru.

Qualification:

B. E. (2003) – HIT, Nidasoshi

Industrial Experience: 17 years

Companies Worked for:

Mobiapps, Nokia, Freescale, Maxlinear

Technologies etc.


Topic Name: Updates in Industry: 5G

Date: 21st December 2020.


Time: 11am -12pm.

ADD-ON COURSES


1. On 13-01-2021, Miss Lavanya Davluri, **Data Scientist-at Product Sampler, Toronto- Canada** made **concluding remarks** to the students on the topic “**Introduction to machine learning using python**”. This event was co-ordinated by Prof. Vinay Chitare, ECE Dept, Prof. Raghavendra Nagaralli, ECE Dept, Prof. Nikhil Kulkarni, ECE Dept. This Add on course was intended to highlight the students with the present developments in the field of machine learning.



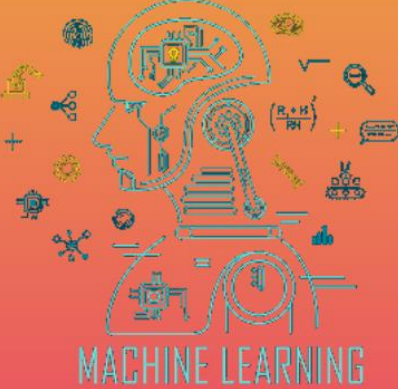
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Department of Electronics and Communication Engineering
Lecture Series on
Introduction to Machine Learning Using Python
By Industry expert



Resource Person
Lavanya Davluri
Data Scientist - Product
Sampler, Toronto, Canada
Our Proud Alumni
2008-2012 Batch



MACHINE LEARNING



Google Meet
Live on

Dr. Arun Kakhandki
HOD

Dr. V.A. Kulkarni
Principal

FDP ATTENDED

Prof. Nikhil Kulkarni of E&CE Department has participated and completed

1. AICTE Training and Learning (ATAL) Academy **Online** FDP on "**Artificial Intelligence**" from **2021-1-4** to **2021-1-8** at **GSSSIETW-Mysuru**
2. AICTE Sponsored Six days online Short-Term Training Program (STTP) on "**Applications of NI Lab View Phase-2**" from **2021-1-25** to **2021-2-1** at **NCET, Bengaluru.**

Faculty Achievement

Congratulations **Prof. Vinay Chitare and Prof. Deepak Sharma** of E&CE Department for clearing VTU-Nov/Dec 2020 ETR.

Faculty Achievements

1. Prof. Vikas Balikai

1) Prof. Vikas Balikai – Asst Prof- Dept of ECE has participated in a two-week Online Faculty Development Programme on “**5G Design: Journey from Devices to Circuits**” jointly organised by Electronics and ICT Academies held from 01 - 12 March, 2021 under the “Scheme of financial assistance for setting up of Electronics and ICT Academies” of the Ministry of Electronics and Information Technology (MeitY), Government of India.

2) Prof. Vikas Balikai has been selected to review journal papers for the following International Journals

- i) International Journal of Electronics published by Taylor and Francis Publications.
- ii) Canadian Journal of Electrical and Computer Engineering published by IEEE.



3) IEEE VDIT student branch conducted a one day Industry webinar on the topic “**Low Power RTL Design**”, on 30-03-2021 at 3:00pm

Resource Person: Mr. Madan Gopal Mekala from StarVLSI Services Company Bangalore

40 Students actively participated in this event with participants from KLS VDIT Haliyal, MIT Manipal, BLDCET Vijaypur, SKSVMACET Laxmeshwar.

Students Achievements

Alumni Achievements in Gate-2021

Sl.NO	Name	Pass Out Batch	Gate Score	Photo
1	Siddesh Gaonkar	2014	1700	
2	Shruti Kunkoor	2017	8226	

PG-Student [2018-2020 batch]

Ms. Ashvini Leele has secured first rank & Gold Medal in M.Tech in Industrial Electronics from VTU Belagavi



Department Activities

Brief of Report on technical talk “Project Identification and Conduction”

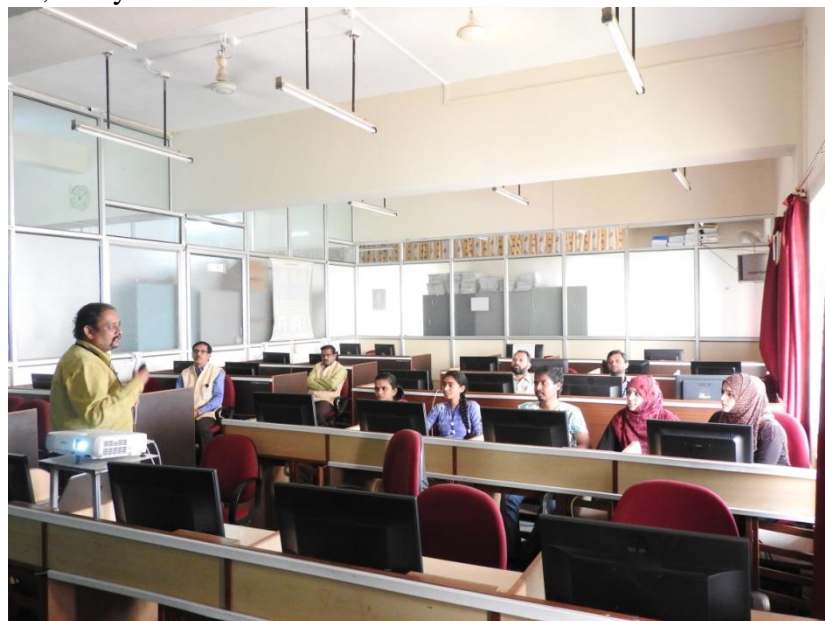
A technical talk on “**Project Identification and Conduction**” was arranged on **20th March 2021** for the first Semester PG Students of Industrial Electronics. The technical talk was delivered by Dr. Mahendra Dixit, HOD (E & CE), KLS VEDIT, Haliyal. In his 2 hours of talk, he has thrown light on various aspects of project work.

He has given various guidelines for the identification and conduction of project work such as

1. Motivation and Introduction to project work:
 - Commit less and work more
 - Play within your limits
 - Objectives should be clear, crisp and concise
2. Classification of projects:
 - Industrial projects
 - Societal relevant projects
 - Trending/ Presently in use technology and many more
3. Tips and Tricks to select projects
 - Existing Projects for improvements
 - Modifications to existing projects
 - New Project Definition

This activity was coordinated by

Prof. A S Joshi
PG Co-ordinator
Industrial Electronics
Dept. Of E & CE,
KLS VEDIT, Haliyal.





EVENTS ORGANIZED IN THE DEPARTMENT



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Web: www.klsvdit.edu.in Email: klsvdrit@yahoo.com / principal@klsvdit.edu.in



Brief of Report on One Day Online Workshop on “LaTeX”

A One Day Hands-on Online workshop on “**LaTeX**” was arranged on **9th April 2021** for all the Faculty Members and PG Students of Industrial Electronics and Thermal Engineering. The List of the Resource persons for the said workshop is as given below.

1) Dr. B. R. Chandavarkar

- Working as an Asst. Prof. in the Dept. of CSE, NITK, Surathkal.

2) Dr. Mohit P. Tahiliani

- Working as an Asst. Prof. in the Dept. of CSE, NITK, Surathkal

3) Dr. Sourav Kanti Addya

- Working as an Asst. Prof. in the Dept. of CSE, NITK, Surathkal

4) Mr. Akhilraj V. Gadagkar

- Research scholar and JRF in Dept. of CSE, NITK, Surathkal

5) Mr. Pradeep Nazareth

- Research scholar in Dept. of CSE, NITK, Surathkal

Prof. A S Joshi

PG Co-ordinator,



KARNATAK LAW SOCIETY'S
VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, HALIYAL



Department of Electronics & Communication Engineering
Presents

One Day Workshop on
“ LaTeX ”
on 9th April 2021



Resource Person
Dr. B R Chandavarkar,
Assoc. Professor,
Department of CSE, NITK,
Surathkal, Mangalore.

Target Audience:
All Faculty and PG
students

Prof. A. S. Joshi
PG Co-ordinator


Dr. M. M. Dixit
H.O.D, ECE

Dr. V. A. Kulkarni
Principal

 www.klsvdit.edu.in


Prof. Vikas Balikai – Asst Prof- Dept of ECE has participated in a two-week Online Faculty Development Programme on “RISC-V VLSI Implementation Flow:RTL2GDS” jointly organised by Electronics and ICT Academies held from 27 March to 10 April, 2021 under the “Scheme of financial assistance for setting up of Electronics and ICT Academies” of the Ministry of Electronics and Information Technology (MeitY), Government of India.

EVENTS ORGANIZED IN THE DEPARTMENT



ಕರ್ನಾಟಕ ನ್ಯಾಯಾಂಗ ಸಂಘಟನೆ
ಅಸ್ತುತು ಕು ವಿಧಾ

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Web: www.klsvdit.edu.in Email: klsvdit@yahoo.com / principal@klsvdit.edu.in



KARNATAK LAW SOCIETY'S
VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY
HALLIYAL

Brief of Report on WEBINAR ON ARTIFICIAL INTELLIGENCE

A “WEBINAR ON ARTIFICIAL INTELLIGENCE – PRESENT AND FUTURE” was organised on 29TH MAY 2021 from 3:00 PM TO 5:00 PM for all the Faculty Members, PG/UG Students and external participants. The Resource person for the said webinar was MR. AJAY KABADI, FOUNDER CEO, DOCKETRUN TECHNOLOGIES PVT. LTD., HUBLI. The event was organised on GOOGLE MEET & the same was FACEBOOK LIVE. The event was coordinated by PROF. PRASANN D. KULKARNI, ECO CO-ORDINATOR, DEPARTMENT OF E&CE. 243 internal and external participants took part in the event.

Karnatak Law Society's
**Vishwanathrao Deshpande Institute
of Technology - Haliyal**



**DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING**

Organizing a Webinar on

**ARTIFICIAL INTELLIGENCE
PRESENT AND FUTURE**

On 29/05/2021 at 3.00PM

Our Guest Speaker:
Shri. Ajay Kabadi
Founder & CEO ,
DocketRun Tech Pvt. Ltd. ,
Artificial Intelligence &
Business Development



Target Audience:
OPEN TO ALL

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Registration



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E-Certificate
after
submission
of feedback

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<https://forms.gle/9ygyXfDcC4YuQYdv8>

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Prof. Prasann D. Kulkarni ECO Co-ordinator	Dr. Mahendra M. Dixit H. O. D. (E&CE)	Dr. V. A. Kulkarni PRINCIPAL
-----------------------------------------------	------------------------------------------	---------------------------------

Prof. Suraj Kadli was invited to be part of 1:1 Virtual Evaluation panel as a jury member for IICDC-2019 which is an event by Texas Instruments in collaboration with Department of Science and Technology (DST), powered by AICTE mission proudly announce the DST & Texas Instruments India Innovation Challenge Design Contest 2019 anchored by NSRCEL, Indian Institute of Management, Bangalore (IIMB) and supported by MyGov.



Prof. Vikas Balikai of the ECE department successfully defended his Ph.D. thesis entitled "**A CMOS implementation of all-digital phase-locked loop**" in the virtual viva examination held on 19-5-2021. He completed his doctorate under the guidance of Dr. Harish Kittur from Vellore Institute of Technology, Vellore. KLS VDIT on behalf of all staff congratulates him on his achievement.

Following students are the top performers in Odd Semester End Examination 2020-21 from Department of Electronics and Communication Engineering

Rank	USN	Name	Percentage
3rd Semester			
1 st	2VD19EC040	LAVANYA AGASIMUNDAL	80.89
2 nd	2VD19EC043	MALLIKARJUN GULUGULI	78.11
3 rd	2VD19EC023	ASHWINI KITTURKAR	77.56
4 th	2VD19EC037	KAVYA A SAVADATTI	76.33
5 th	2VD19EC075	SHRADDHA V BOLISHETTY	76.11
5th Semester			
1 st	2VD18EC109	VANI TADAKOD	83.33
2 nd	2VD18EC015	DEEPTI KAMALAKAR MAHALE	82.89
3 rd	2VD18EC093	SONAM VITTHAL MULE	82.22
4 th	2VD18EC027	KIRTHI A MATHAD	82.11
5 th	2VD18EC105	TRUPTI SURESH GHATAGE	81.89
7th Semester			
1 st	2VD17EC023	MUSKAN M SHAIKH	86.50
2 nd	2VD17EC056	SAMREEN PATEL	83.63
3 rd	2VD17EC007	APOORVA S SUNKAD	82.00
	2VD17EC090	VIDYASHREE C	82.00
4 th	2VD17EC029	NISHA RAIKAR	81.25
5 th	2VD17EC004	AMRUTA DATTATREYA JOSHI	81.00

WEBINARS / WORKSHOPS CONDUCTED

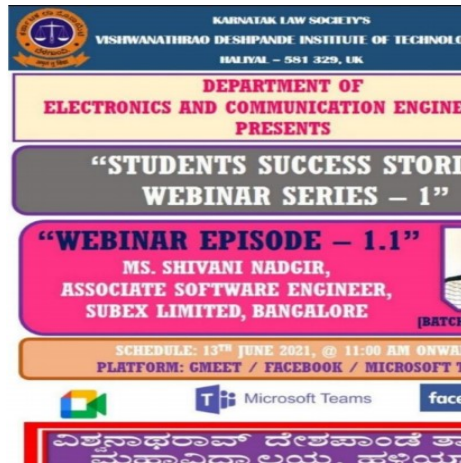
1. Department of E&CE and E&EE in association with Electronic Circuit Design Club of Innovation Forum, KLS Vdit Haliyal on A “One day online workshop on PCB Design using Industry Standard Tool EAGLE EDA Software” was conducted on 1st June 2021, This event was coordinated by Prof. Deepak Sharma, Asst. Prof E&CE Dept, KLS’s Vdit, The Resource Person was Dr. Subhas Meti, HOD-ECE, SKSVMACET, Laxmeshwar.



2. Department of ECE in association with IEEE student Branch of Vdit conducted a webinar on “Exploring Data Science” for all the student of Vdit was conducted on 21st June 2021, Dr. Jyoti Munavalli Associate Professor, Dept. of ECE, BNMIT, Bengaluru was the resource person and the Event Coordinated by Prof. Vikas Balikai Asst.Prof E&CE Dept, KLS’s Vdit.

The poster is for a webinar titled "Exploring Data Science" organized by the Department of Electronics & Communication Engineering in association with the IEEE Student branch. It features a photo of Dr. Jyoti Munavalli, Associate Professor, Dept of ECE, BNMIT Bengaluru. The event is on 21-06-2021 at 3:00 pm on the MS teams platform. The poster also includes the logo of Karnatak Law Society's Vishwanathrao Deshpande Institute of Technology, Haliyal, and a QR code for registration. The registration link is <https://forms.gle/3fhkasVsbwF93wWYA>. The poster also features the "Exploring" logo and icons for Data Science, AI, and ML.

3. Department of E&CE, KLS's Vdit, Haliyal Conducted "Students Success Stories Webinar Series - 1 – Episode 1.1, on 13th June 2021, In this event Miss. Shivani Nadgir [Alumni E&CE Dept] , Subex Technologies, Bengaluru was the resource person and the event was coordinated by Prof. Deepak Sharma, Asst.Prof E&CE Dept, KLS's Vdit.



4. Department of E&CE, KLS's Vdit, Haliyal Conducted "Students Success Stories Webinar Series - 1 – Episode 1.2, on 20th June 2021, In this event Miss. Kavya Manu, IBM Pvt. Ltd., Bengaluru was the resource person and the event was coordinated by Prof. Deepak Sharma, Asst.Prof E&CE Dept, KLS's Vdit



5. Department of E&CE, KLS's Vdit, Haliyal Conducted "Students Success Stories Webinar Series - 1 – Episode 1.3, on 27th June 2021, In this event Miss. Jyothi C., TCS, Bengaluru was the resource person and the event was coordinated by Prof. Deepak Sharma, Asst.Prof E&CE Dept, KLS's Vdit



WEBINARS / WORKSHOPS / FDP / TECHNICAL TALK ATTENDED

1. Prof. Rohini K. Asst. Prof , E&CE Dept, KLS's VBIT, Haliyal
 - i. has successfully completed the online Webinar on "NAAC Accreditation" held on 10th June, 2021 organized by Department of Civil Engineering under IQAC, Shreeyash College of Engineering & Technology, Aurangabad.
 - ii. has attended One Day Online Workshop on "One day online workshop on PCB Design using Industry Standard Tool EAGLE EDA Software" was conducted on 1st June 2021, organized by department of E&CE and E&EE in association with Electronic Circuit Design Club of Innovation Forum, KLS VBIT Haliyal.
2. Following Faculty Members of E&CE Dept, KLS's, VBIT, Haliyal have participated in Webinar on "Counselling, What, Why & How....?", Organised by Internal Quality Assurance Cell (IQAC), KLS's VBIT Haliyal on 23/06/ 2021.

Sl. No	Name of the Faculty Members	Sl. No	Name of the Faculty Members
1.	Dr. Mahendra M. Dixit	9.	Prof. Prasanna D. Kulkarni
2.	Dr. Arun L. Kakhandki	10.	Prof. Shree Gowri. S.S.
3.	Prof. Kalmesh M. Waderhatti	11.	Prof. Vinay Chitare
4.	Prof. Avinash S Joshi Kulkarni	12.	Prof. Nikhil Kulkarni
5.	Prof. Basavaraj D. Goudar	13.	Prof. Vikas Balikai
6.	Prof. . Damodar Hotkar	14.	Prof. Rohini Kallur
7.	Prof. Plasin F Dias	15.	Prof. Raghavendra Nagaralli
8.	Prof. Virapax M. Chougala	16.	Prof. Meenaxi Torase

3. Prof. Avinash S. Joshi, Asst. Prof , E&CE Dept, KLS's VBIT, Haliyal
 - i. has successfully completed the online Webinar on "NAAC Accreditation" held on 10th June, 2021 organized by Department of Civil Engineering under IQAC, Shreeyash College of Engineering & Technology, Aurangabad.
 - ii. has participated in the Tech Talks on "Simulation Of Power Electronics Circuits Using MATLAB" organized under IEEE PELS DAY Celebration 2021 by IEEE SB Panimalar Institute of Technology, Chennai on 11 June 2021.

FACULTY ACHIEVEMENT

1. Prof. Vikas Balikai was invited as a reviewer on June 12, for the IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics to be held on Nov 19, 2021, in NMAMIT Nitte, Karnataka.
2. Prof. Vikas Balikai was invited as a reviewer on June 6, 2021, for a manuscript in the International Journal of Electronics – a Taylor and Francis Publication International Journal.
3. Prof. Vikas Balikai's research paper was accepted on June 21, 2021, for oral presentation in IEEE MSS Ph.D. Colloquium to be held in the month of July 2021.
4. Prof. Vikas Balikai was invited as reviewer and External Panel jury for I2CONECCT (Industry Institute Conclave on Electronics, Computational and Communication Technologies) an intercollegiate project exhibition and competition, under Robotics, Automation, Vehicular Technology and Alternate Energy Theme. This is organized by IEEE Mangalore Subsection and Sahyadri College of Engineering & Management College. Event is on 26-06-2021 in online mode.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
JULY MONTH ACTIVITY REPORT

➤ **ALUMNI TALK SERIES**

1. On 10th July 2021, Mr. **AMIT SOMANAGOUDAR, APPLICATION ENGINEER SIEMENS EDA, BANGALORE** addressed to the students of 4th and 6th semester on the topic “**AN OVERVIEW OF PCB INDUSTRY: CONCEPT TO MANUFACTURING**”. This event was Co-ordinated by **Prof. Plasin Dias** and **Prof. Vinay Chitare**, ECE Dept.

Karnatak Law society's
Vishwanathrao Deshpande Institute of Technology, Haliyal
Department of Electronics and Communication Engineering
In Association with IEI Students' Chapter
Alumni Talk Series #6
An Overview Of PCB Industry: Concept To Manufacturing
Target audience
4th & 6th Sem
Live on
Google Meet
Resource Person
AMIT SOMANAGOUDAR,
Application Engineer,
Siemens EDA, Bangalore
Our Proud Alumni
2007-2011 Batch
Free Registration
Date : 10 July 2021 , Time : 11:30AM - 12:30PM
Prof Plasin Dias
Prof Vinay Chitare
Alumni Co-ordinators
Dr. Mahendra M. Dixit
HoD, E&CE
Dr. V. A. Kulkarni
Principal

➤ **FDP ATTENDED**

Dr. Arun L. Kakhandki of E&CE Department has participated and completed

1. Attended AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "**FPGA Based Deep Learning Applications in Signal Processing**" from 2021-07-05 to 2021-07-09 at KLE Technological University, Hubli.
2. Attended AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "**Digital Forensics and Investigations**" from 19-7-2021 to 23-7-2021 at K.L.E Technological University, Hubli.

Prof. Rohini R. Kallur of E&CE Department has participated and completed

1. Participated in Faculty Development Programme on “**Research Trends in VLSI, Communications & Signal Processing**”, organized by Department of Electronics and Communication Engineering, Bapatla Women’s Engineering College, Bapatla, Andhra Pradesh during 8th to 10th July, 2021.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
JULY MONTH ACTIVITY REPORT

➤ **FDP ATTENDED**

Prof. Plasin F. Dias of E&CE Department has participated and completed

1. Participated in Faculty Development Programme on “**Research Trends in VLSI, Communications & Signal Processing**”, organized by Department of Electronics and Communication Engineering, Bapatla Women’s Engineering College, Bapatla, Andhra Pradesh during 8th to 10th July, 2021.

Prof. Deepak Sharma of E&CE Department has Participated and completed

1. AICTE Training And Learning (ATAL) Academy Online Elementary FDP on “**Hands-on approach for the Design and Implementation of Lab-on-Bread Board- A Blended learning**” from 2021-7-21 to 2021-7-25 at The National Institute of Engineering Mysore.

Prof. Rahul C.M of E&CE Department has Participated and completed

1. AICTE Training And Learning (ATAL) Academy Online Elementary FDP on “**Hands-on approach for the Design and Implementation of Lab-on-Bread Board- A Blended learning**” from 2021-7-21 to 2021-7-25 at The National Institute of Engineering Mysore.

➤ **Faculty Achievements**

Prof. Rahul C. M. and Prof. Deepak Sharma of E&CE Dept. **Conducted the Workshop on "Hands-on Arduino for IOT Application" from 15th July 2021 to 16th July 2021 for 2nd Year BCA Students at KLS’s BCA College, Haliyal.**





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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

JULY MONTH ACTIVITY REPORT

➤ **Webinars Organized**

1. “STUDENTS SUCCESS STORIES WEBINAR SERIES – 1, WEBINAR EPISODE - 1.4”

1. On 4th July 2021, **MS. ANUSHREE K. KUBSAD, ASSOCIATE SOFTWARE ENGINEER, TECH MAHINDRA, BANGALORE** addressed to the students of 4th and 6th semester on the topic **“Her Experience In College, Her Experience In Interview, Preparation Strategy And Interview Skills Motivation For All Placed And Unplaced Students”**. This event was Co-ordinated by **Prof. Deepak Sharma, Prof. Rahul C. M., T&P Coordinator, Department of E&CE.**

2. On 11th July 2021, **Ms. NIKITHA G. C., PROGRAMMER ANALYST TRAINEE, COGNIZANT TECHNOLOGY SOLUTIONS, BANGALORE** addressed to the students of 4th and 6th semester on the topic **“Her Experience In Cognizant Interview, Interview Skills, Preparation Strategy For Doing Higher Studies – MS”**. This event was Co-ordinated by **Prof. Deepak Sharma, Prof. Rahul C. M., T&P Coordinator, Department of E&CE.**

KL SV DIT
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HALIYAL - 581 329, KA.

DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
PRESENTS

“STUDENTS SUCCESS STORIES
WEBINAR SERIES – 1”

“WEBINAR EPISODE – 1.4”
MS. ANUSHREE K. KUBSAD,
ASSOCIATE SOFTWARE ENGINEER,
TECH MAHINDRA, BANGALORE
APPOINTMENT TYPE : OFF-CAMPUS (JULY 2021 - 2021)

SCHEDULE: 04th JULY 2021 @ 11:00 AM ONWARDS
PLATFORM: FACEBOOK / MICROSOFT TEAMS

Microsoft Teams facebook

ವಿಶ್ವನಾಥರಾವ್ ದೇಶಪಾಂಡೆ ತಾಂತ್ರಿಕ
ಮಹಾವಿದ್ಯಾಲಯ, ಹಳಿಯಾಳ

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DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
PRESENTS

“STUDENTS SUCCESS STORIES
WEBINAR SERIES – 1”

“WEBINAR EPISODE – 1.5”
MS. NIKITHA G. C.,
Programmer Analyst Trainee,
Cognizant Technology Solutions,
BANGALORE (JULY 2021 - 2021)

SCHEDULE: 11th JULY 2021 @ 11:00 AM ONWARDS
PLATFORM: FACEBOOK / MICROSOFT TEAMS

Microsoft Teams facebook

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ಮಹಾವಿದ್ಯಾಲಯ, ಹಳಿಯಾಳ

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