Karnatak Law Society's Vishwanathrao Deshpande Institute of Technology, Haliyal - 581329

Rev. Dt: 06/12/202

DEPARTMENT ACADEMIC CALENDAR (ODD SEMESTER : 3rd, 5th & 7th Sem B.E.) w.c.f. 19-9-2022

AY: 2022-23 Semester: ODD (3rd, 5th & 7th SEMSTER B.E.)

VISION:

To bring out talented, skilled, and sustainable Electronics and
Communication Engineering Graduates through strong domain
expertise to serve the Society with greater Professional Ethics

MISSION:

† To create and impart an active learning ambience to accomplish a high degree of Professional competencies.

† To inculcate innovative research and developmental thinking in effective Teaching and Learning processes for solving Societal challenges.

† To deliver the needs and requirements of the latest state of art of the Industry through quality multidisciplinary internship and training processes.

expertise	to serve	the Society with greater Professional Ethics					To deliver the needs and require				ements of the latest state of art of the Industry through quality multidisciplinary internship and training programs.			
Month	WEEK (Sem 7)	WEEK (Sem 5)	WEEK (Sem 3)	MON	TUE	WED	THU	FRI	SAT	SUN	Dates	Activities		
PT 22	1			19	20	21	22	23	24	25	19	Commencement of 7th Semester		
SEPT 2022	2			26	27	28	29	30						
OCT - 2022	2				505-3 107-10101A				1	2	2	Gandhi Jayanti		
											3	Monthly Activity Report / Academic Progress Report / Sept Month Attendance Report to 7th Sem Parents		
	3			3	4	5	6	7	8	9	4 & 5 8	Maha Navami / Ayudh Pooja & Vijayadashami		
											9	A Online Webinar by Alumni is arranged by Prof. Plasin Dias Maharshi Valmiki Jayanti		
	4	1		10	11	12	13	14	15	16	10 & 15	Commencement of 5th Semester & Graduation Day		
	5	2		17	18	19	20	21	22	23	21 & 22	Short term training program on Basics of Python for 5th Semester Students by Prof. Rohini K & Prof. Shreegowri S S		
											24 & 26	Naraka Chaturdashi & Balipadhya / Deepavali		
	6	3		24	25	26	27	28	29	30	27, 28, 29 29	IA-1 for 7th Semester Hands on session-1 on basic modulations using hardware for 5th Semester Students by Prof.		
												Basavaraj D		
	7	4	1	31							31	Commencement of 3rd Semester		
	_					_	_				3	Kannada Rajyotsava Monthly Activity Report / Academic Progress Report / October Month Attendance Report to 5th & 7th		
	7	4	1		1	2	3	4	5	6	5	Sem Parents / IA-1 Marks to 7th Sem Parents Technical Talk for UG/PG Students is arranged by Prof. A. S. Joshi		
)22					0	_	10		30	10	11	Kanakadasa Jayanti		
- 2022	8	5	2	7	8	9	10	11	12	13	12	A Online Webinar by Alumni is arranged by Prof. Plasin Dias		
NOV	9	6	3	14	15	16	17	18	19	20	19	Hands on session-2 on basic modulations using hardware for 5th Semester Students by Prof.  Basavaral D G		
ž	10	7	4	21	22	23	24	25	26	27	24, 25, 26	IA-1 for 5th Semester & IA-2 for 7th Semester		
				00							28	Hands on Session on Multisim for 3rd Semester Students by Prof Robini K & Prof. Shreegowri S S		
	11	8	5	28	29	30					30	IA-1 Marks to 5th Sem Parents / IA-2 Marks to 7th Sem Parents		
DEC - 2022	11	8	5				1	2	3	4	3	Monthly Activity Report / Academic Progress Report /		
	11	0	J				1	2	3	4	4	November Month Attendance Report to 3rd, 5th & 7th Sem Parents  Parents Meet (for 5th & 7th Sem Semesters)		
	12	9	6	5	6	7	8	9	10	11	10	Use of Math-Transform in Real Time Systems for Faculty Members is arranged by Prof. Suraj Kadli		
	13	10	7	12	13	14	15	16	17	18	17	A Webinar on Use of Signal Processing in Embedded Systems is arranged by Prof. Suraj K		
	14	11	8	19	20	21	22	23	24	25	23-24 25	Alumni Meet / Annual Gathering		
											26, 27, 28	Christmas IA-1 for 3rd Sem; IA-2 for 5th Sem & IA-3 for 7th Semester		
	15	12	9	26	27	28	29	30	31		31	IA-1 Marks for 3rd Semester Parents & IA-2 for 5th Semester Parents IA-3 & Final Marks to 7th Sem Parents / Last working day of 7th Semester		
JAN - 2023	15	12	9							1				
											4	Monthly Activity Report / Academic Progress Report / December Month Attendance Report to 3rd, 5th & 7th Sem Parents /One Day Hands on Workshop on Applications of Op amp for 5th Semester Students		
		13	10	2	3	4	5	6	7	8	7	is arranged by Prof. Rahul M & Prof. Deepak S		
								Ŭ				IA-1 Marks to 3rd Sem Parents / A Technical Webinar is arranged by Prof. Raghavendra N for 7th Semester Students		
											Я	Parents Meet (for 3rd Sem Semesters)		
									No.		9	One Day Workshop on Basic Acquaintance with Electronic Components and Equipment for 3rd		
		14	11	9	10	11	12	13	14	15	44	Semester Students is arranged by Prof. Rahul M & Prof. Deepak S		
											14	A Technical Talk is arranged by Prof. Raghavendra N for 7th Semester Students		
		15	12	16	17	18	19	20	21	22	16, 17, 18	IA-2 for 3rd Semester & IA-3 for 5th Semester		
											23 26	IA-2 Marks to 3rd Semester Parents & IA-3 Marks to IA-3 & Final Marks to 5th Semester Parents Republic Day		
		16	13	23	24	25	26	27	28	29	23-27 & 27	Lab IA for 5th Sem / Last working day of 5th Semester		
								1						
			14	30	31									
FEB-2023					- 1						1, 2, 3 3	IA-3 for 3rd Semester Monthly Activity Report / Academic Progress Report / January Month Attendance Report to 3rd & 5th		
			14			1	2	3	4	5	4	Sem Parents		
											11	Staff Day  Last working day of 3rd Semester		
ᄄ			15	6	7	8	9	10	11	12	6-11 8 / 11	Lab IA & Additional Theory IA to eligible 3rd Sem students / International Conference		
			T. 184. S. S. S.				rnship to					IA-3 & Final Marks to 3rd Sem Parents / Final Attendance to 3rd Sem Parents 22 to 30-10-2022		
Internship to THIRD Semester (Lateral Entry) BE:											26-03-2023 to 16-04-2023			
				w-1000	Practica		hip to SE ations o				21-08-2022 to 17-09-2022 13-02-2023 to 21-02-2023			
Practical Examinations of THIRD Semester BE :  Practical Examinations of FIFTH Semester BE :											13-02-2023 to 21-02-2023 30-01-2023 to 09-02-2023			
				Pr			ons of SE				03-01-2023 to 13-01-2023			
Theory Examinations of THIRD Semester BE : Theory Examinations of FIFTH Semester BE :											22-02-2023 to 22-03-2023 13-02-2023 to 18-03-2023			
												16-01-2023 to 15-02-2023		
				Commend							17-04-202	3, 20-03-2023 & 20-02-2023		
1st & 3rd	1 Saturdo	y of the	month s	vill be ted	ach & led	ırn from	home at	the disc	retion of	HOI.				

Head of the Department
Dept. of Electronic & Communication Engg.

KLS VDIT, HALIYAL

Principal KLS Vishwanathrao Deshpande