

Karnatak Law Society's												
Vishwanathrao Deshpande Institute of Technology, Haliyal - 581 329												
Doc. No.: VDOT/ACAD/TT/01										Rev.No.:02		
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DEPARTMENTAL MASTER TIME TABLE												
Department: Electronics & Communication Engg												
AY: 2022-23												
ODD Semester												
DAY	Dept.	SEM	Block No	9:00 - 10:00	10:00 - 11:00	11 - 11:15	11:15 - 12:15	12:15 - 1:15	1:15 - 2:00	2:00 - 3:00	3:00 - 4:00	
MONDAY	ECE	3A	1S15	21EC34	21EC34		Add - On	Add - On		21EC32	21EC32	
		3B	1S13	21EC32	21EC32		21EC33	21EC33		B1 : 21ECL35 / B2 : 21EC381 / B3 : MATDIP		
		5A	1S11	18EC52	18EC52		18EC54	18EC54		18ES51	18CIV59	
		5B	1S9	18EC53	18EC53		B2 : 18ECL57 / B1 : 18ECL58			Add - On	Add - On	
		7A	1F18	A1 : 18ECL76 / A2 : 18ECL77			18EC72	18EC72		18ECP78		
		7B		18EC71 (6)	18EC71 (6)		18EC743 (10)	18EC733 (10)		18ECP78		
TUESDAY	ECE	3A	1S15	21EC32	21EC32		21MAT31	21MAT31		A2 : 21ECL35 / A3 : 21EC381		
		3B	1S13	21MAT31	21MAT31		21UH36	MATDIP		21EC33	21EC33	
		5A	1S11	18EC56	18EC56		18ES51	18ES51		18EC53	18EC54	
		5B	1S9	18EC54	18EC54		18EC52	18EC52		18EC56	18CIV59	
		7A	1F18	OE	OE		A2 : 18ECL76 / A3 : 18ECL77			18ECP78		
		7B		OE	OE		18EC72 (6)	18EC71 (6)		ENGLISH (6)	ENGLISH (6)	
WEDNESDAY	ECE	3A	1S15	21MAT31	21MAT31		21EC34	21EC34		A3 : 21ECL35 / A1 : 21EC381		
		3B	1S13	B2 : 21ECL35 / B3 : 21EC381			21EC34	21EC33		21EC32	21EC32	
		5A	1S11	18EC53	18EC53		A2 : 18ECL57 / A1 : 18ECL58			18EC52	Course Mini - Project Work	
		5B	1S9	18EC52	18EC52		18EC55	18EC55		18EC53	18ES51	
		7A	1F18	18EC733	18EC733		18EC71	18EC71		ENGLISH	ENGLISH	
		7B		B1 : 18ECL76 / B2 : 18ECL77			ADD - ON (9)	ADD - ON (9)		18ECP78		
THURSDAY	ECE	3A	1S15	21EC33	21CIP37		21EC33	21EC33		21EC32	Seminar / Webinar	
		3B	1S13	Add - On	Add - On		B3 : 21ECL35 / B1 : 21EC381			21MAT31	21MAT31	
		5A	1S11	A1 : 18ECL57 / A2 : 18ECL58			18EC52	18EC52		18EC56	18EC55	
		5B	1S9	18ES51	18ES51		18EC53	18EC53		18EC52	18EC54	
		7A	1F18	18EC743	18EC743		18EC71	ADD - ON		A3 : 18ECL76 / A1 : 18ECL77		
		7B		18EC743 (9)	18EC743 (9)		B2 : 18ECL76 / B3 : 18ECL77			18ECP78		
FRIDAY	ECE	3A	1S15	21EC33	21EC33		A1 : 21ECL35 / A2 : 21EC381			21UH36	Seminar / Webinar	
		3B	1S13	21EC34	21EC34		21EC32	21CIP37		Seminar / Webinar		
		5A	1S11	18EC55	18EC55		18EC53	18EC53		Add - On	Add - On	
		5B	1S9	B1 : 18ECL57 / B2 : 18ECL58			18EC56	18EC56		18EC55	Course Mini - Project Work	
		7A	1F18	ADD - ON	ADD - ON		18EC743	18EC72		18ECP78		
		7B		18EC72 (10)	18EC72 (10)		18EC733 (7)	18EC733 (7)		B3 : 18ECL76 / B1 : 18ECL77		
SATURDAY	ECE	3A	1S15	ADD - ON	ADD - ON		21MAT31	21EC34		Seminar / Webinar	NSS	
		3B	1S13	21MAT31	21EC34		Add - On	Add - On		Seminar / Webinar	NSS	
		5A	1S11	18EC55	18EC56		Add - On	Add - On		Course Mini - Project Work		
		5B	1S9	Add - On	Add - On		18EC55	18EC56		Course Mini - Project Work		
		7A	1F18	OE	OE		18EC733	18EC733		18ECP78		
		7B		OE	OE		ADD - ON (AC)	ADD - ON (AC)		18ECP78		

HoD 31.10.2022

Head of the Department
Dept. of Electronic & Communication Engg.
KLS V.D.I.T., HALIYAL (U.K.)

Dean, Academics
KLS VDOT, HALIYAL

Principal

Principal
KLS Vishwanathrao Deshpande
Institute of Technology, Haliyal

		Subject code	Subject title	Staff allotted	Subject code	Subject title	Staff allotted
3rd Semester	Maths	21MAT31	Transform Calculus, Fourier Series And Numerical Techniques	Dr. SPH / Dr. LL	21CIP37	Constitution of India and Professional Ethics	Mrs. VU
	ECE	21EC32	Digital System Design using Verilog	Prof. RK	21EC381	LD Lab using Pspice / MultiSIM	Prof. NK / Prof. DS
		21EC33	Basic Signal Processing	Prof. PD	21NS83	National Service Scheme	
		21EC34	Analog Electronic Circuits	Prof. RCM / Prof. NK	Add - On	Algorithmic Approach to Solve Complex Problems	Prof. VMC / Prof. BG
		21ECL35	Analog & Digital Electronics Lab	Prof. RCM / Prof. ASJ /	21MATDIP31	Additional Mathematics - I	Dr. LL
		21UH36	Social Connect and Responsibility	Prof. BG			
5th Semester	ECE	18ESS1	Technological Innovation Management and Entrepreneurship	Prof. VMC	18EC56	Verilog HDL	Prof. SS
		18EC52	Digital Signal Processing	Prof. SK	18ECL57	Digital Signal Processing Laboratory	Prof. KMW / Prof. SK
		18EC53	Principles of Communication Systems	Prof. BG	18ECL58	HDL Laboratory	Prof. RK / Prof. SS
		18EC54	Information Theory & Coding	Prof. RN	Add on	Basics of Machine Learning	Prof. PD / Prof. KMW
		18EC55	Electromagnetic Waves	Prof. VC			
	CE	18CIV59	Environmental Studies	Prof. RP			
7th Semester	ECE	18EC71	Computer Networks	Dr. ALK	18xx75x	Open Elective	
		18EC72	VLSI Design	Prof. DS	18ECL76	Computer Networks Lab	Prof. RN / Prof. DH / Prof. VMC
		18EC733	Digital Image Processing	Prof. DH	18ECL77	VLSI Laboratory	Prof. DS / Prof. VC
		18EC743	Multimedia Communication	Prof. RN / Prof. ASJ	18ECP78	Project Work Phase - 1	
		Add-On	Basics of Antenna Design using HFSS	Prof. ASJ / Prof. RN			

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