



Karnatak Law Society's  
**VISHWANATHRAO DESHPANDE  
INSTITUTE OF TECHNOLOGY, HALIYAL**

(Recognized Under Section 2(f) of UGC Act, 1956)  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Department of Electronics & Communication Engineering.**  
**February 2023**  
**Conference / FDP / Training / Meeting / Webinars Attended and  
Conducted:**

- 1. Dr. Mahendra M. Dixit**, Professor and Head has represented KLS Vdit & Participated as Subject Expert in Five Days Workshop entitled "**Trilingual Glossary in Engineering (English-Hindi-Kannada)**" organized by CSTT, Department of Higher Education, MoE, New Delhi held at **RVCE** Bangalore from **January 30, 2023 to February 03, 2023**
- 2. Dr. Mahendra M. Dixit**, Professor and Head, **Prof. Deepak Sharma** has filed a Patent for the work entitled as "**COTTONMITRA – A MAJOR FUNGAL FOLIARDISEASE DETECTION AND CLASSIFICATION TECHNIQUE IN COTTON LEAF**" on **February 24, 2023**
- 3. Dr. Mahendra M. Dixit**, Professor and Head, **Prof. Deepak Sharma** has filed a Patent for the work entitled as "**PUNICAVACHA – A MAJOR DISEASE DETECTION AND CLASSIFICATION TECHNIQUE IN POMEGRANATE LEAF**" on **February 27, 2023**
- 4. Prof. Damodar Hotkar** was a reviewer in the IEEE ICICACS 2023 International Conference Organized by Department of E&CE of H.K.E Society's S.L.N College of Engineering, Raichur, India in association with IEEE Bangalore and North Karnataka Sub Section from **February 24, 2023 to February 25, 2023**

## Distinguished Alumni



Ms. Priyadarshini H.  
Graphics Hardware Engineer,  
Intel Technology Pvt Ltd, Bangalore

Ms. Priyadarshini H has Completed Bachelor of Engineering in Electronics and Communication Engineering in June 2016 VBIT, Haliyal. Presently she is working as a Graphics Hardware Engineer, Intel Technology Pvt Ltd, Bangalore India.