

**KLS Vishwanathrao Deshpande Institute of Technology, Haliyal - 581 329**

Doc. No.: VDIT/ACAD/TT/4a

Rev.No.:02

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Rev. Dt: 09/12/2020

DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING 3rd SEM TIME TABLE

ODD SEMESTER

With effect from: 18-09-2024

AY:2024-2025

DAY	SEM	Block No	09:00 - 10:00	10:00 - 11:00	11:00 - 11:15	11:15 - 12:15	12:15 - 01:15	01:15 - 02:00	02:00 - 03:00	03:00 - 04:00
Monday	3	1F11	BEE304	BEE306A	S B h r o e r a t k	BEE303	BEE301	L B u r n e c a h k	302(B1)/303(B2)/305(B3)	
Tuesday			BEE301	BEE302		BEE304	A O C		302(B2)/303(B3)/305(B1)	
Wednesday			BEE303	BEE302		BEE304	BEE306A		302(B3)/303(B1)/305(B2)	
Thursday			BEE301	BEE302		BEE303	BEE306A		BEE301	A O C
Friday			BEEL358C			A O C	Tutorial		BSCK307	
Saturday										

Batches	R. No.s
B1	1 TO 22
B2	23 TO 44
B3	45 Onwards

Subject Code	Subject Title	Subject Alloted
BEE301	Engineering Mathematics	Prof. Akshata Patil
BEE302	Electrical Circuit Analysis	Prof. Varaprasad G
BEE303	Analog Electronic Circuits	Prof. Ravindra M
BEE304	Transformers and Generators	Prof. S M Hegde
BEEL305	Transformers and Generators Lab	Prof. S M Hegde
BEE306A	ESC - (Digital Logic Circuits)	Prof. S M Inamadar
BSCK307	Social Connect & Responsibility	Prof. S M Inamadar
BEEL358C	AEC - (Circuit lab Using PSPICE)	Prof. Ravindra M
ADEE03	A O C (Automotive Electronics)	Dr.Arun L K

H.O.D.

HEAD
Dept. of Electrical & Electronics Engg.
KLS's V. D. Institute of Technology
HALIYAL-581 329.

Dean-Academic

Dean, Academics
KLS VDIT, HALIYAL

Principal

Principal
KLS Vishwanathrao Deshpande
Institute of Technology, Haliyal