

	KLS Vishwanathrao Deshpande Institute of Technology, Haliyal - 581 329	
	Doc. No.: VDIT/ACAD/TT/4a	Rev.No.:02
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	DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING 5th SEM TIME TABLE	

ODD SEMESTER

With effect from: 09-09-2024

AY:2024-2025

DAY	SEM	Block No	09:00 - 10:00	10:00 - 11:00	11:00 - 11:15	11:15 - 12:15	12:15 - 01:15	01:15 - 02:00	02:00 - 03:00	03:00 - 04:00
Monday	5	1F13	BEE515A	BEE503	S B h r o e r a t k	BEE502(B1)/BEE504(B2)		L B u r n e c a h k	BEE586	
Tuesday			BEE503	BEE501		BEE502(B2)/BEE504(B3)			Tutorial	
Wednesday			BEE515A	BEE502		BEE501	BEE503		BRMK557	AOC
Thursday			BEE501	BEE515A		BEE502	AOC		BEE502(B3)/BEE504(B1)	
Friday			BEE502	BEE503		BRMK557	AOC		BESK508	
Saturday										

Batches	R. No.s
B1	1 TO 22
B2	23 TO 44
B3	45 TO 65

Subject Code	Subject Title	Subject Alloted
BEE501	Engineering Management and Entrepreneurship	Prof. Deepak L
BEE502	Signals & DSP	Prof. Sheetal M
BEE503	Power Electronics	Prof. Kirankumar H
BEE504	Power Electronics Lab	Prof. Kirankumar H
BEE515A	PE-High Voltage Engineering	Prof.Manjunatha
BEE586	Mini Project	Prof. Sheetal M
BRMK557	Research Methodology and IPR	Prof.Deepak L
BESK508	Environmental Studies	Prof.Rakesh P
	AOC (P L C)	Prof.Rajeshwari N


H.O.D.

HEAD

Dept. of Electrical & Electronics Engg.
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HALIYAL-581 329.


Dean Academics
KLS VBIT, HALIYAL

Principal


Principal
KLS Vishwanathrao Deshpande
Institute of Technology, Haliyal