



AICTE ATAL EduSkills

A Faculty Development Program
on

“MICROCHIP EMBEDDED SYSTEMS”

from 5th May - 10th May 2025



Organized by

Department of
Electrical and Electronics Engineering

KLS VEDIT, HALIYAL

Accredited by NAAC with “A” Grade

Approved by A.I.C.T.E., New Delhi, Affiliated to V.T.U., Belagavi
(Recognised Under Section 2(f) by UGC, New Delhi)

Udyog Vidya Nagar, Dandeli Road, Haliyal-581329
Karnataka, India.

www.klsvdit.edu.in

ABOUT THE INSTITUTE

Karnatak Law Society having established a number of institutions in the city of Belagavi which have identity as Quality Educational Institutions started “Vishwanathrao Deshpande Institute of Technology” (VDIT) at Haliyal in 2004-05 by the initiation and support of Shri. R. V. Deshpande former minister Government of Karnataka. Within a short span of years, KLS VDIT has developed excellent infrastructural facilities and an academic climate to train the students to meet the challenges posed by rapid changing techno-economic scenario. The institution has an excellent, well qualified and experienced faculty. The Institute is located on a 26 acres campus, cradled by a picturesque landscape.

ABOUT THE DEPARTMENT

The Department of Electrical & Electronics Engineering was established in the year 2004 with an intake of 60. The department has well-qualified staff with expertise in Power Systems, Electronics and Computer Applications, assisted by skilled supporting staff. The department has present state of art laboratories to meet the requirements of University curriculum. Electrical Staff and Student Association (ELECTRA)-a departmental association provides a platform for students to exhibit their talents in various extracurricular activities. fields. The Department has MoUs with reputed organisations

ABOUT THE FDP

The objective of the FDP is to impart Knowledge of Microchip Platform Microcontroller devices. The FDP will benefit the faculty members to carry out research in Embedded Systems Development through Microchip devices and also offer quality projects to the students. The FDP will be conducted as a boot camp with 30% theory and 70% hands-on activities with real time use cases.

Topics to be covered

General Embedded System

8 Bit Microcontroller

16 Bit Microcontroller

32 Bit Microcontroller

Low Power Design

Question & Answers



OUTCOMES OF FDP

- **Enhanced Knowledge and Skills:** Faculty members gain a deeper understanding of Microchip's technologies, tools, and methodologies, including the latest advancements in embedded systems development.
- **Improved Teaching Abilities:** They acquire new teaching techniques and pedagogical approaches tailored to embedded systems education, enabling them to deliver more engaging and effective courses.

Mode of Conduction: OFFLINE

CHIEF PATRON

Shri. Anant Mandagi
President, KLS Belgaum.

PATRONS

Shri. R. V. Deshpande

CONVENER

Dr. V. A. Kulkarni
Principal, KLS VDIT Haliyal

HEAD OF THE DEPARTMENT

Prof. Rajeshwari Nanannavar
Assistant Professor, KLS VDIT Haliyal

RESOURCE PERSON

Mr. Prabhupada Samal
Assistant Manager
Learning & Development
EduSkills Foundation

COORDINATOR

Prof. Subrahmanya Hegde
Assistant Professor, Department of EEE
KLS VDIT Haliyal
Contact No: 7760372312

Prof. Ravindra Motekar
Assistant Professor, Department of EEE
KLS VDIT Haliyal
Contact No: 9738472638

