



# KLS Vishwanathrao Deshpande Institute of Technology

(Accredited by NAAC with "A" Grade)  
(Approved by AICTE, New Delhi. Affiliated to VTU, Belagavi)  
(Recognized Under Section 2(f) by UGC, New Delhi)  
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## Department of Computer Science & Engineering Hands on Session on "Digital Circuit Design"

### Pre event

Date: 13/11/2025

Venue: EEE Dept, LD LAB, KLS VEDIT Haliyal

Resource person name & affiliation: Prof. Shree Gowri S S, Asst. Prof., KLS VEDIT Haliyal

Beneficiaries: 65 Students of 3<sup>rd</sup> semester CSE department.

### Objective of the Handson Session:

End of the session students will be able to

1. Explain the fundamental concept of Digital Circuits
2. Designing the Digital Circuit using Hardware Components.
3. Implementation and working of Digital Circuits.

### PO mapping:

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
2	1												2

### Feedback questions:

1. Did the Hands-on Session addressed fundamentals concepts of the Designing Digital Circuits. (PSO2)
2. Did the Hands-on Session addressed explanation of kits, ICs etc (PO1)
3. Was the demonstration of circuit wiring and testing helpful? (PO2)

  
Event Coordinator

  
HoD

  
Principal

HOD  
Computer Science & Engineering  
KLS Vishwanathrao Deshpande  
Institute of Technology, Haliyal  
Post event

### Feedback summary:

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
2.5	2.6												2.6

### PO attainment:

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
2.4	1.15												2.3

  
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